

Inductive Data Flow Graphs

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January 23, 2013

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Given a (concurrent) program P and a specification $\varphi_{\text{pre}}/\varphi_{\text{post}}$, prove

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(or provide a counter-example)

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This talk presents **Inductive Data Flow Graphs** (iDFGs): a form of correctness proof for (concurrent) programs

Why iDFGs?

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- **Succinct**
 - Present only the *essence* of a proof
 - Polynomial in the data complexity of a program
- Can be **generated** and **checked automatically**
 - Extend **static analysis** to concurrent control
 - Extend **model checking** to (unbounded) data

“Essence” of a proof

$$\varphi_{\text{pre}} : x \geq 0 \wedge y \geq 0$$

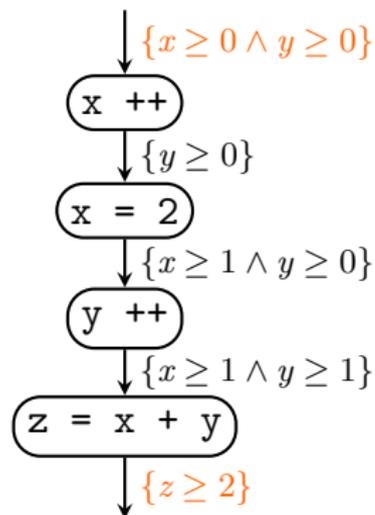
Thread 1

$x \ ++$
 $y \ ++$
 $z = x + y$

Thread 2

$x = 2$

$$\varphi_{\text{post}} : z \geq 2$$



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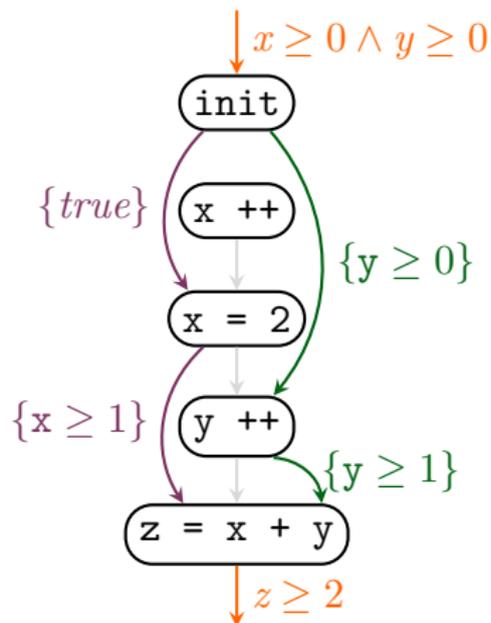
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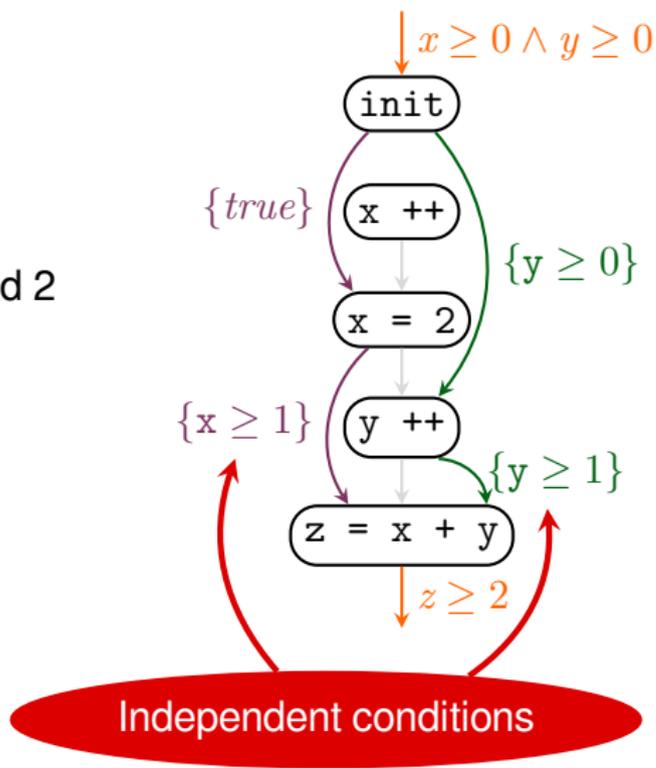
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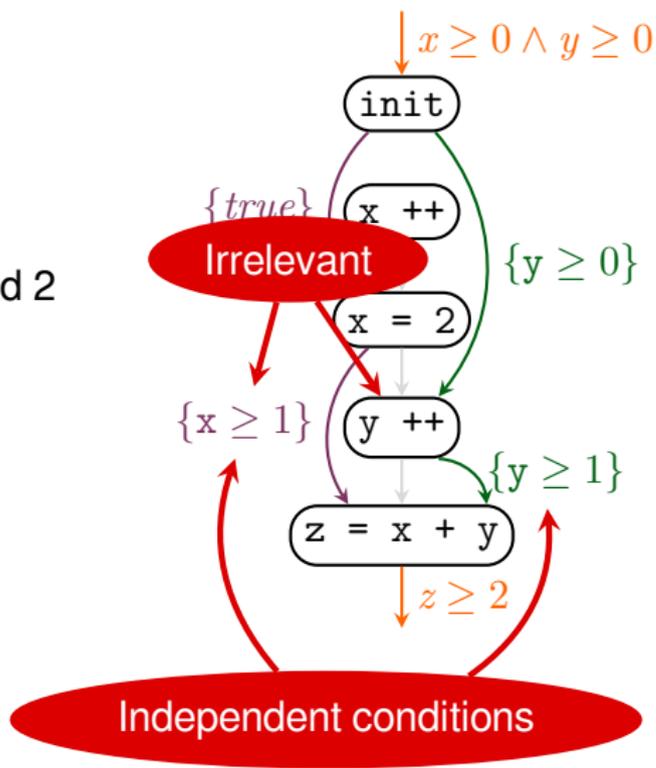
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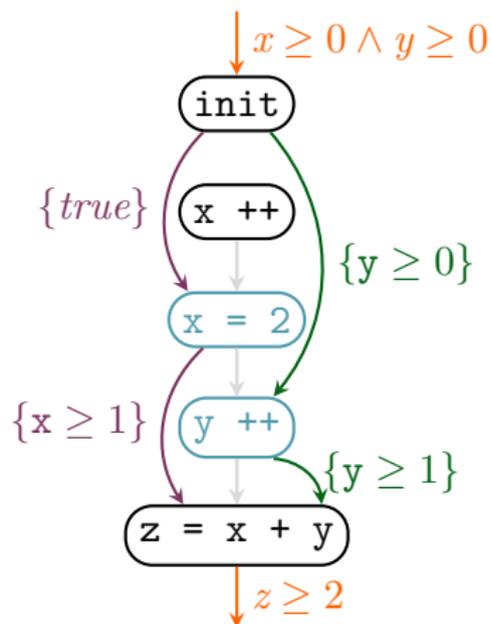
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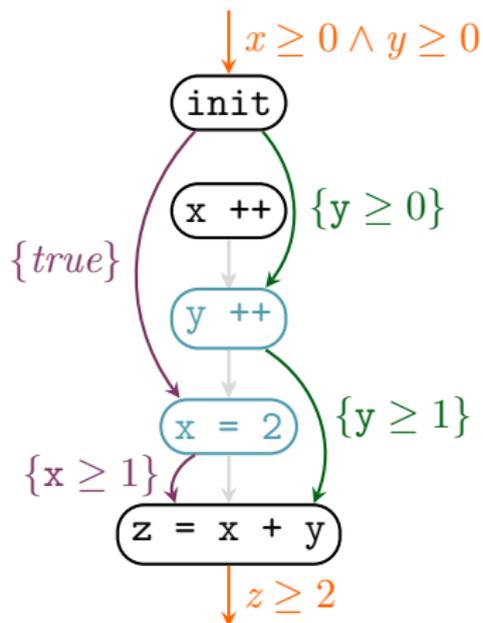
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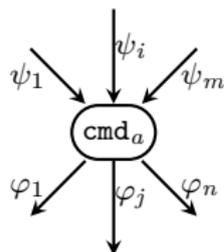
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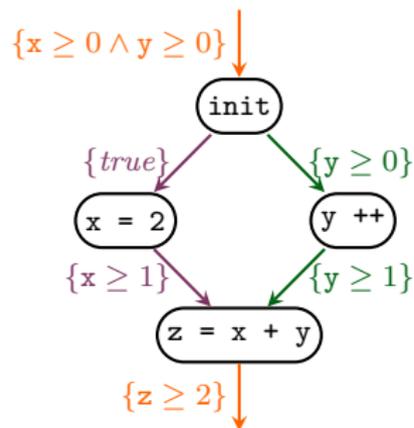


Inductive Data Flow Graphs (iDFGs)

Inductiveness condition:



for all j , $\{\psi_1 \wedge \dots \wedge \psi_m\} \text{cmd}_a \{\varphi_j\}$

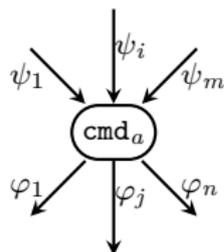


Suppress irrelevant details of a partial correctness proof

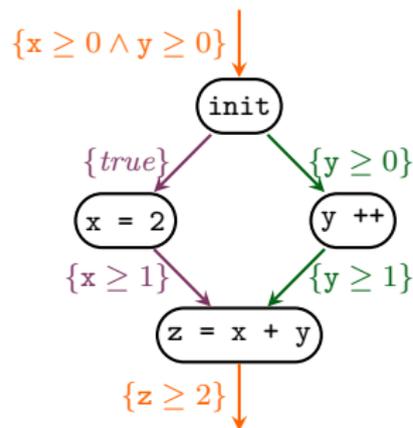
- Irrelevant ordering constraints
($x = 2; y ++$ vs $y ++; x = 2$)
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Parallelize a partial correctness proof

- Irrelevant ordering constraints

($x = 2; y ++$ vs $y ++; x = 2$)

- Irrelevant actions ($x ++$)

Denotation of an iDFG

Data flow graph with inductive assertions (iDFG) proves correctness of traces that obey particular constraints

~ Control flow graph with inductive assertions (Floyd annotation) proves correctness of traces that label paths

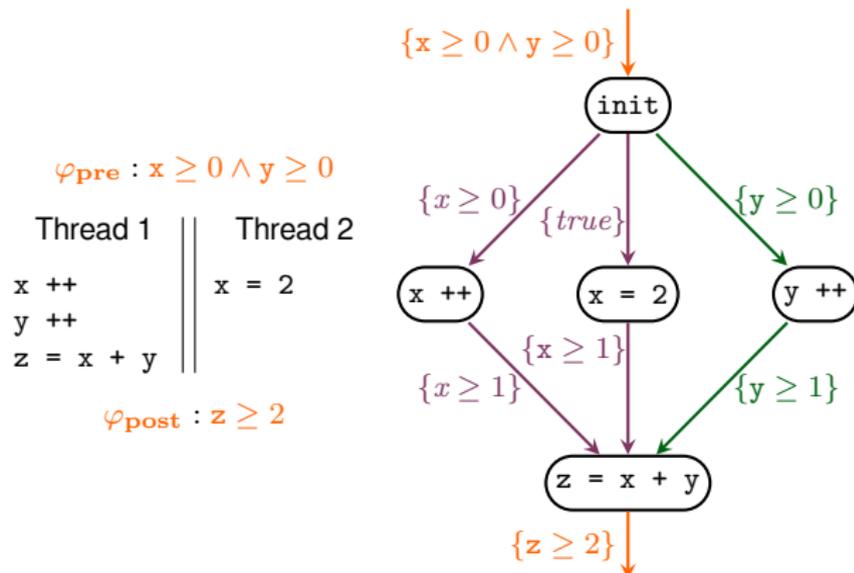
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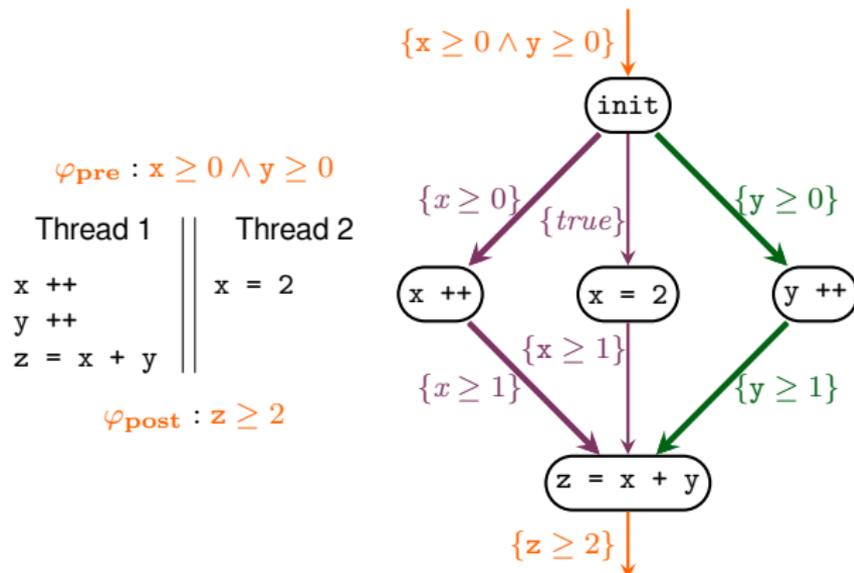


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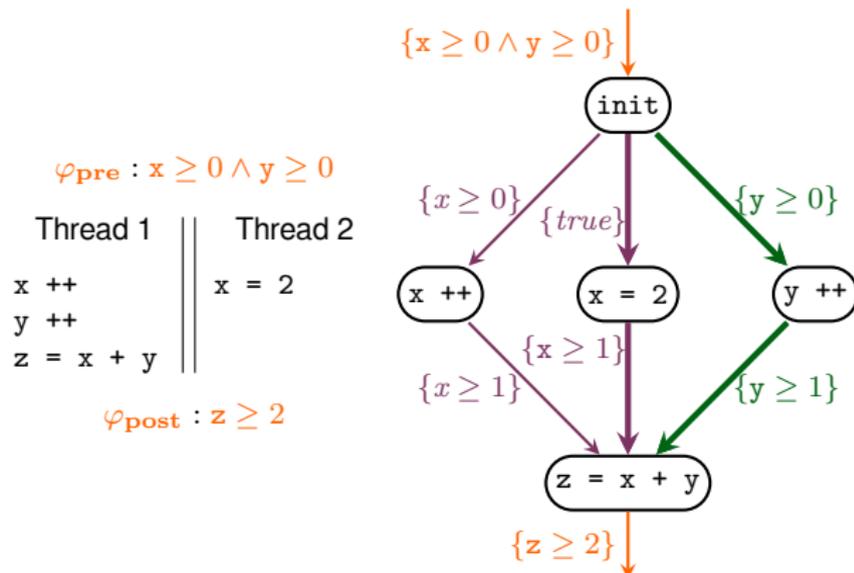


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Theorem

Let $G = \langle V, E, \varphi_{\text{pre}}, \varphi_{\text{post}}, v_o, V_{\text{final}} \rangle$ be an iDFG. For all $\tau \in \llbracket G \rrbracket$,

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Proof rule

Program P is correct w.r.t. $\varphi_{\text{pre}}/\varphi_{\text{post}}$ iff $\exists G. \mathcal{L}(P) \subseteq \llbracket G \rrbracket$

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For any $\{\varphi_{\text{pre}}\}P\{\varphi_{\text{post}}\}$, there exists a iDFG proof with size polynomial in the *data complexity* of $P, \varphi_{\text{pre}}, \varphi_{\text{post}}$

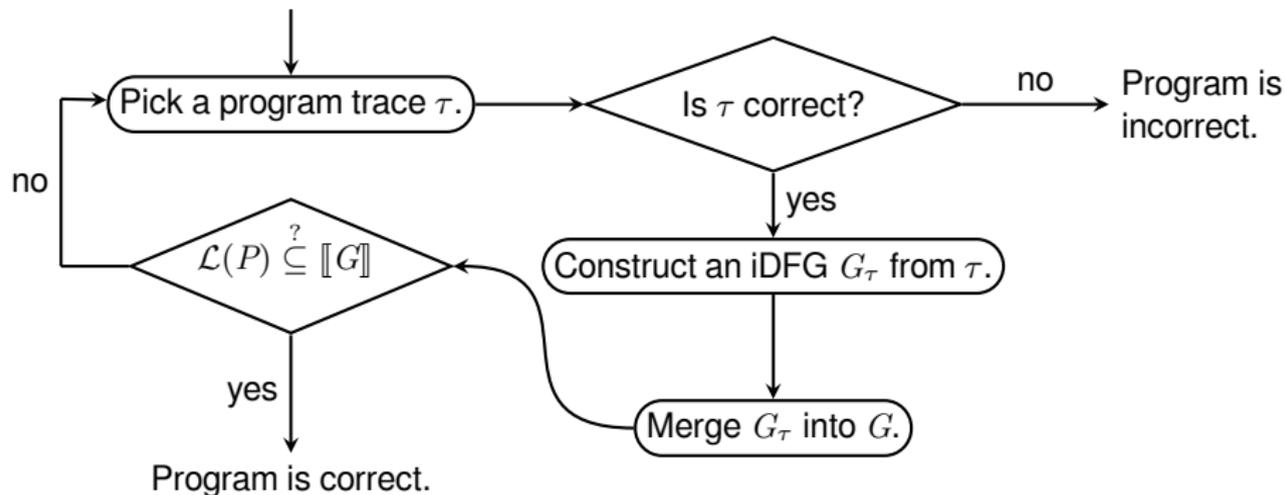
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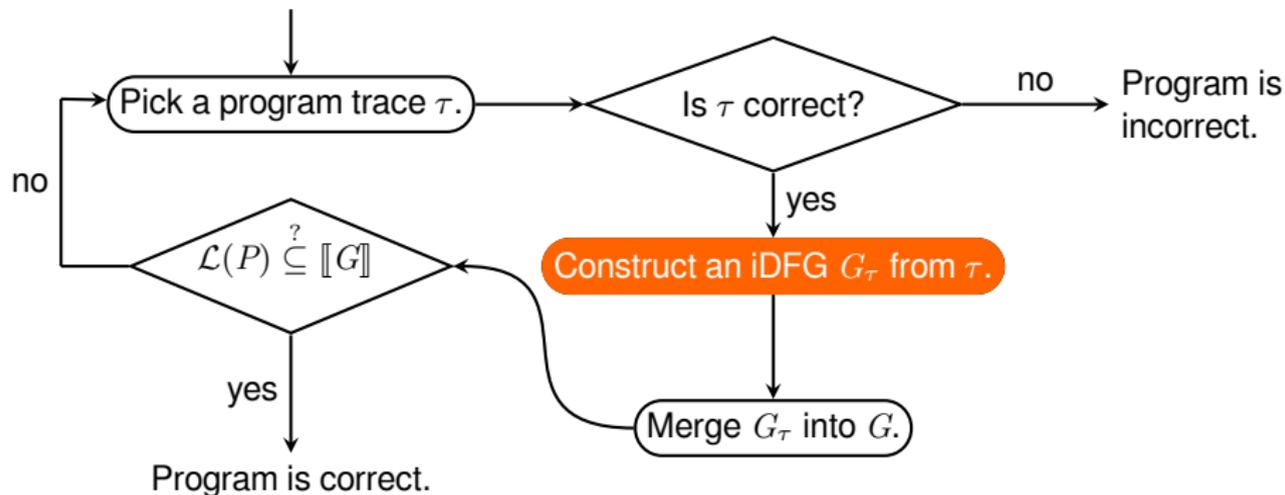
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Data complexity measures how difficult a property is to prove.

- Minimum # of assertions in a *localized proof* that $\{\varphi_{\text{pre}}\}P\{\varphi_{\text{post}}\}$
 - Localized proofs: expose “*how compositional*” a Floyd proof is.

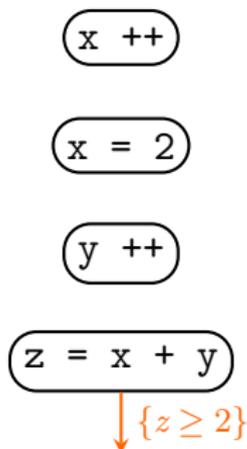


Automation



Goal

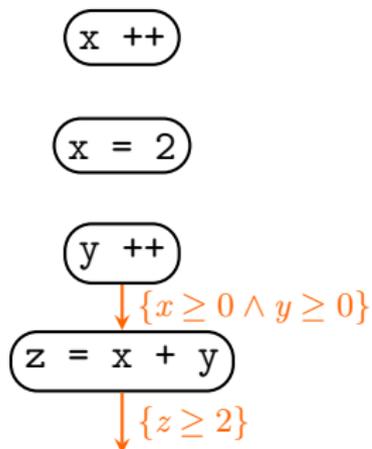
Given a trace $\tau \in \mathcal{L}(P)$ with $\{\varphi_{\text{pre}}\}_\tau\{\varphi_{\text{post}}\}$, construct an iDFG G_τ with $\tau \in \llbracket G_\tau \rrbracket$.



iDFG construction

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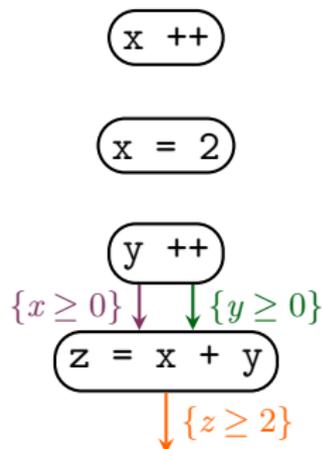
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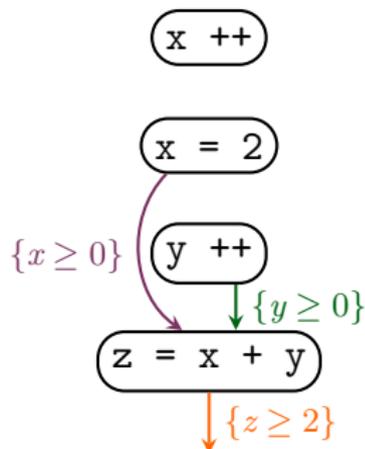
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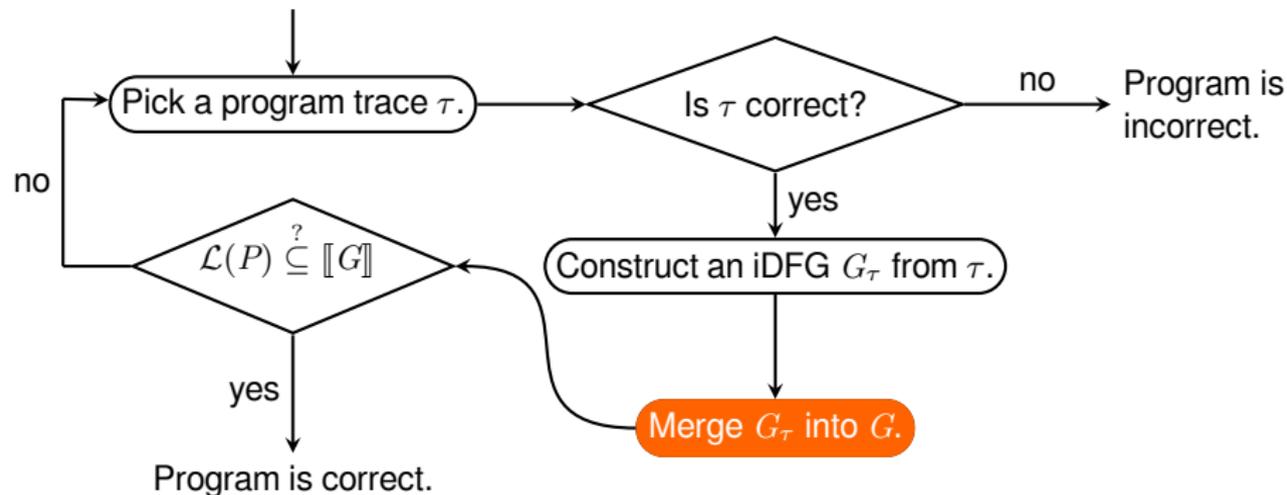


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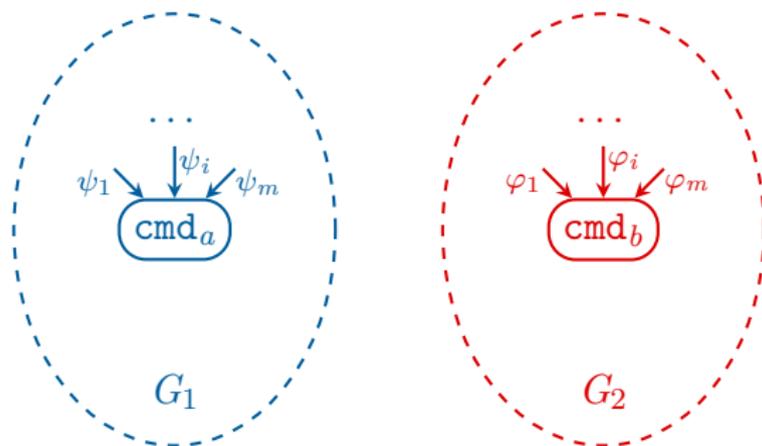


Merging iDFGs

Goal

Given iDFGs G_1 , G_2 , construct an iDFG $G_1 \bowtie G_2$ such that

$$\llbracket G_1 \rrbracket \cup \llbracket G_2 \rrbracket \subseteq \llbracket G_1 \bowtie G_2 \rrbracket$$

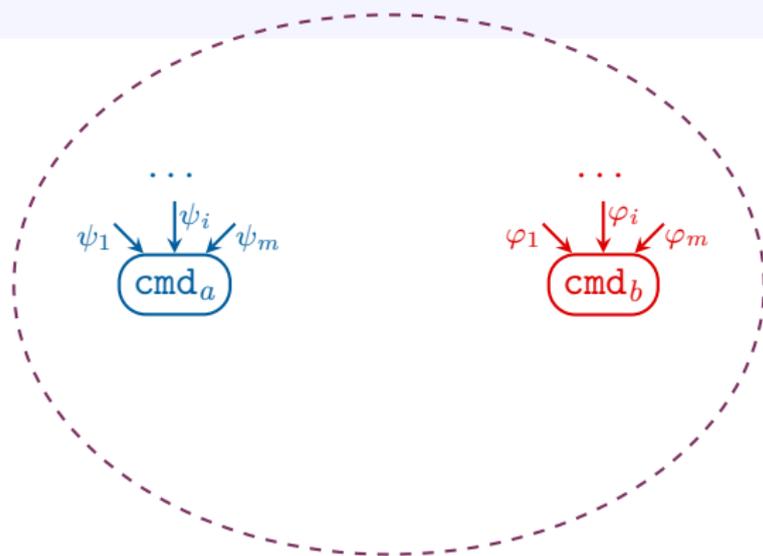


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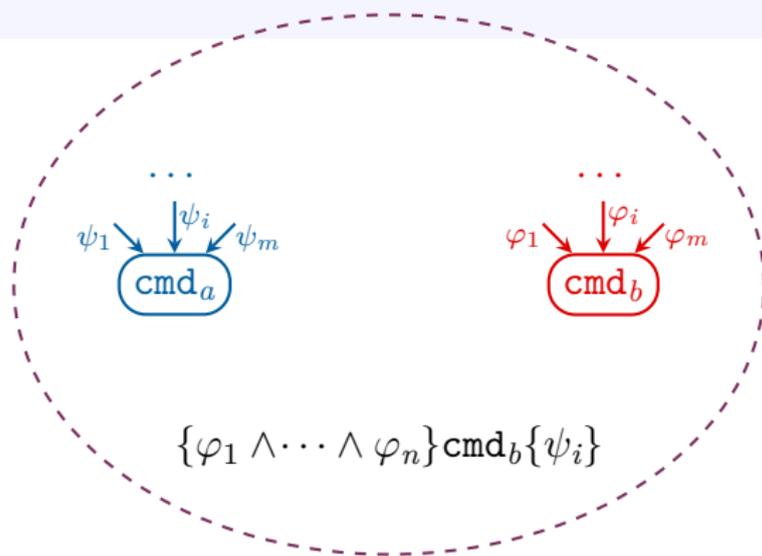


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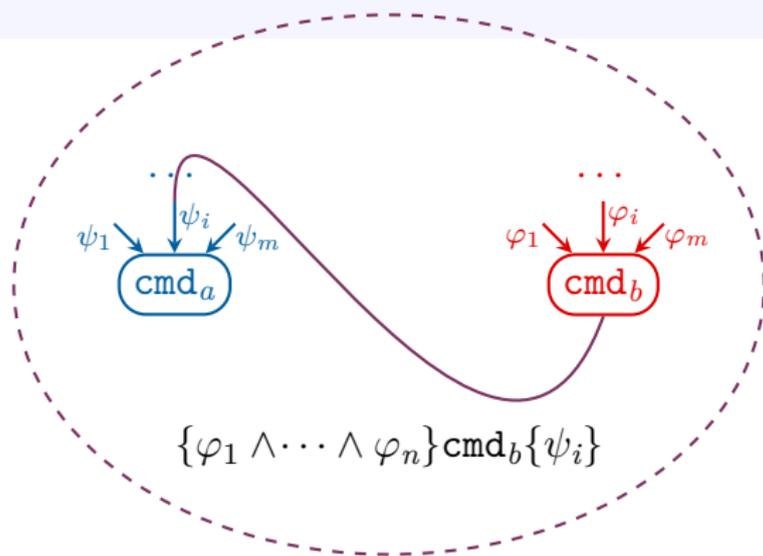


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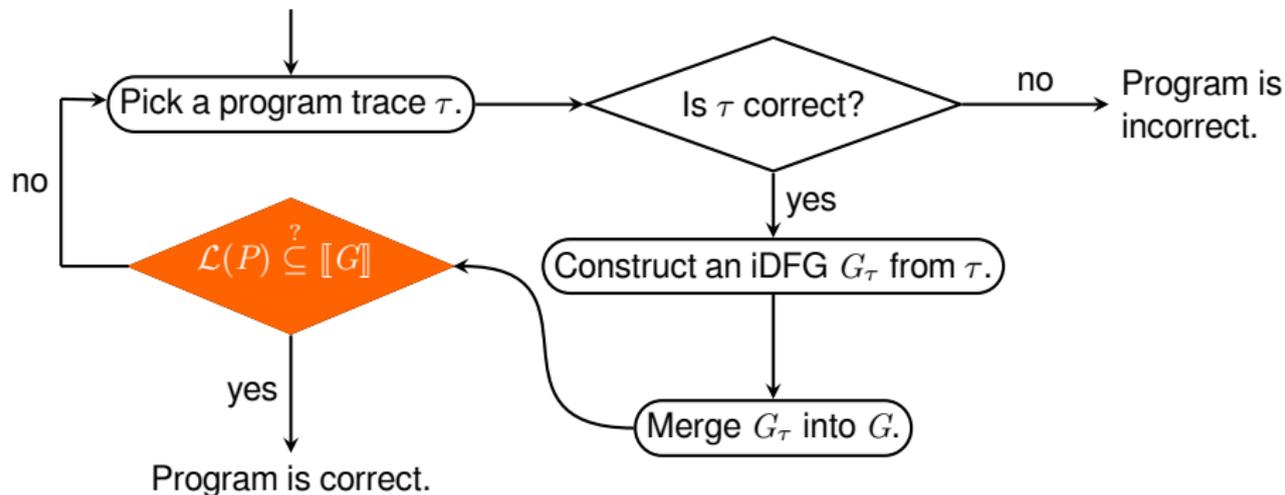
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Automation



For any iDFG G , we can efficiently (linear time, in the size of G) construct an *alternating finite automaton* A_G such that

$$\mathcal{L}(A_G) = \llbracket G \rrbracket^{rev}$$

Proof checking: $\mathcal{L}(P)^{rev} \stackrel{?}{\subseteq} \mathcal{L}(A_G)$

- Can be solved in PSPACE
- **Combinatorial** problem (non-reachability)
- Reuse techniques from (finite-state) model checking

- **Inductive Data Flow Graphs** are a proof method for partial correctness of (concurrent) programs
- (Provably) **succinct**
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Summary

- **Inductive Data Flow Graphs** are a proof method for partial correctness of (concurrent) programs
- (Provably) **succinct**
- Can be **generated automatically**

Future work

- Can iDFGs be constructed more effectively?
- Efficient proof checking?
- Parameterized programs?
- Weak memory models?

Thank you for your attention.