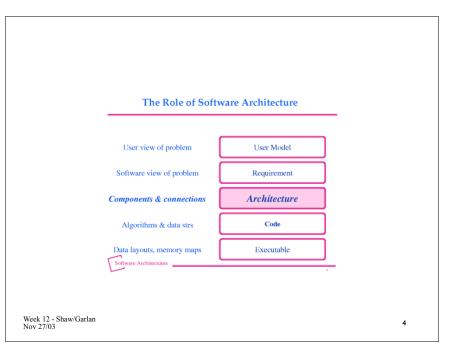
	Part I: Architectural Design
Architecture	I. Architectural design: planning system structure > What is software architecture? > What is its relation to other aspects of design? II. The variety of software architectures: common styles III. Deciding which architecture to use break
Mary Shaw and David Garlan	IV. Concrete examples V. Dealing with mismatched parts VI. Topics of possible future interest
Taken from Tutorial on Architectures for Software Systems http://spoke.compose.cs.cmu.edu/shaweb/p/pubs.htm	Software Architectures6
Week 12 - Shaw/Garlan Nov 27/03	Week 12 - Shaw/Garlan Nov 27/03

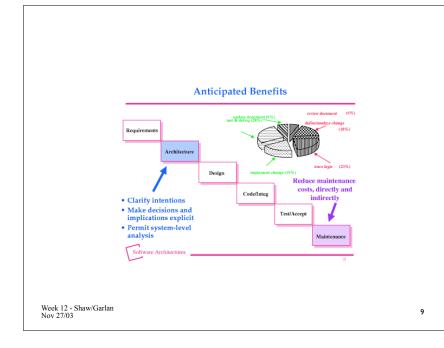
<ul> <li>Descriptions of software systems</li> <li>Usually informal prose plus box- architecture of this system"</li> <li>Usually informal prose plus box- and-line diagram</li> <li>Lots of appeal to intuition</li> <li>Little precision, rarely formal</li> </ul>	-
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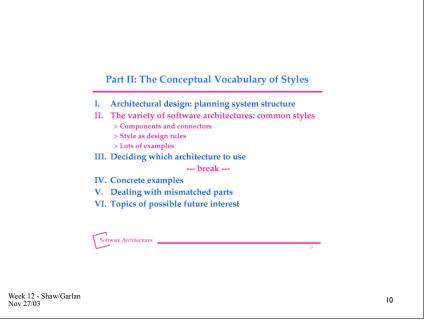


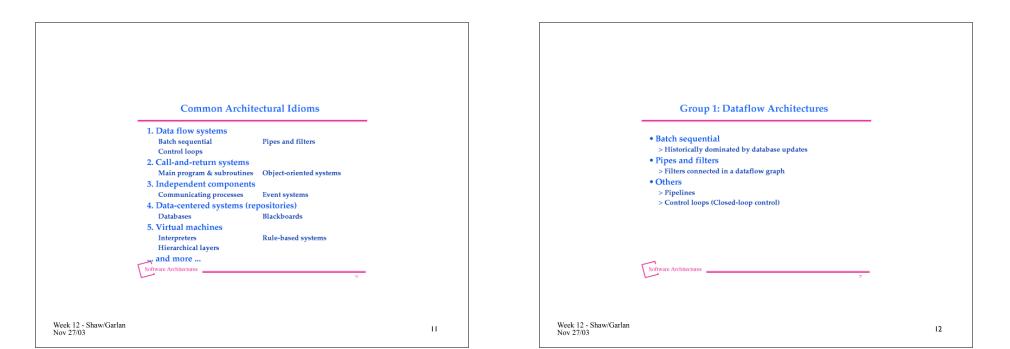
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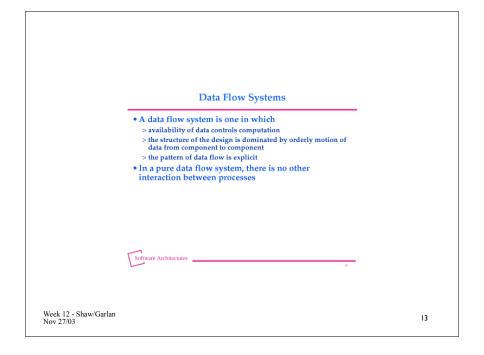
Architectural Design Task Different issues for architecture & programs interactions among parts structural properties declarative mostly static system-level performance outside module boundary composition of subsystems	ies ce ry ies	<ul> <li>Typical Descriptions of Software Architectures</li> <li>"Camelot is based on the client-server model and uses remote procedure calls both locally and remotely to provide communication among applications and servers." [Spector 87]</li> <li>"We have chosen a distributed, object-oriented approach to managing information." [Linton 87]</li> <li>"The casiest way to make the canonical sequential compiler into a concurrent compiler is to pipeline the execution of the compiler phases over a number of processors." [Seshadri 88]</li> <li>"The ARC network [follows] the general network architecture specified by the ISO in the Open Systems Interconnection Reference Model." [Paulk 85]</li> </ul>	
Week 12 - Shaw/Garlan Nov 27/03	5	Week 12 - Shaw/Garlan Nov 27/03	6

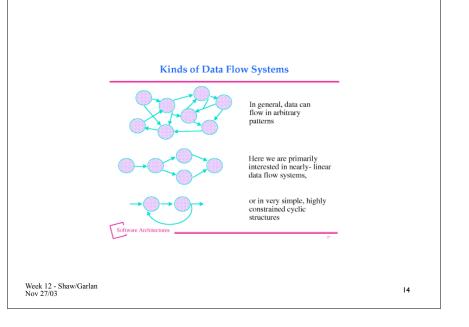


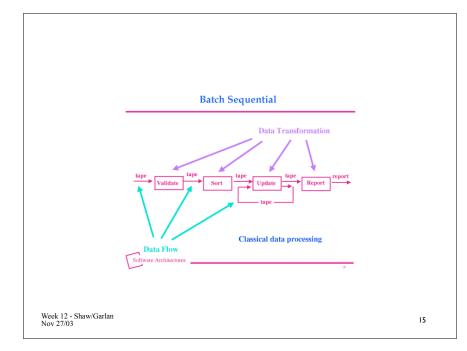


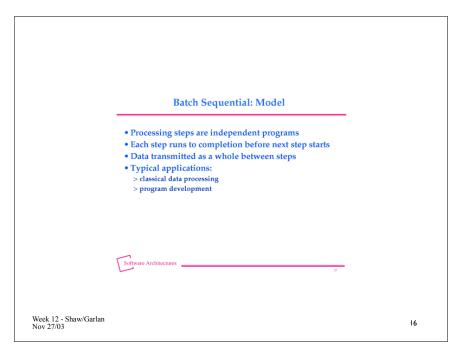


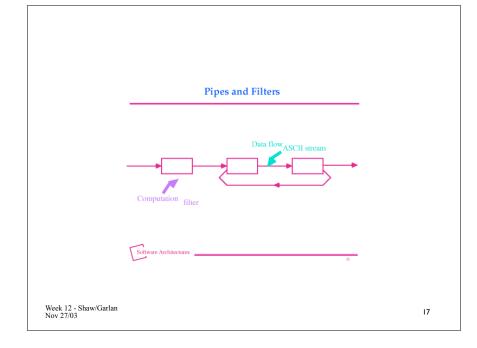


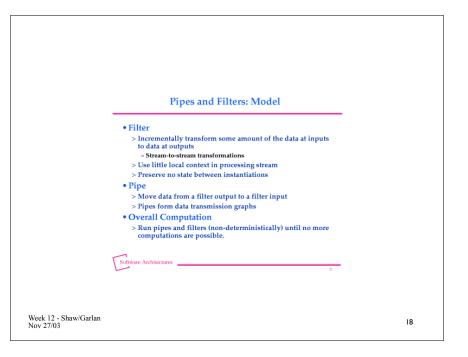


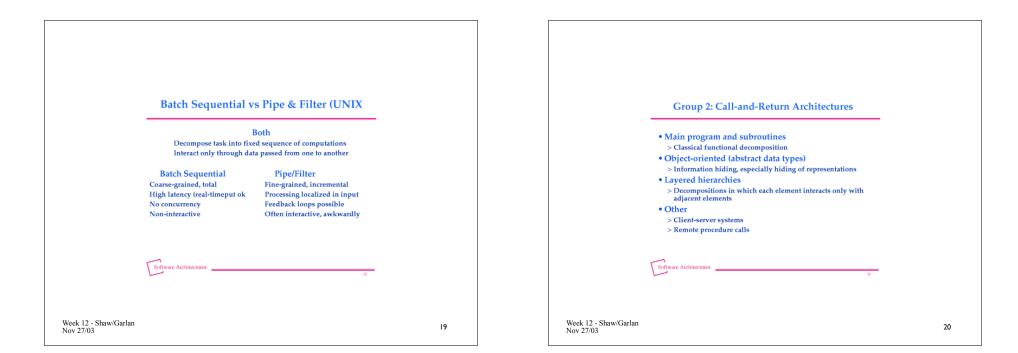


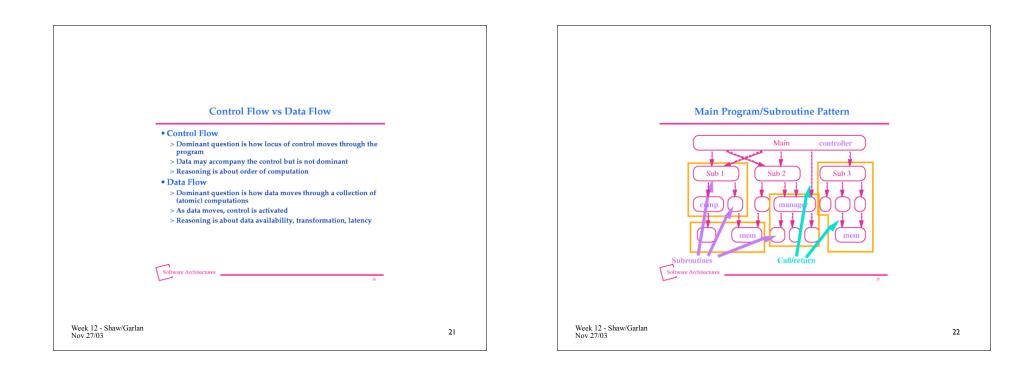


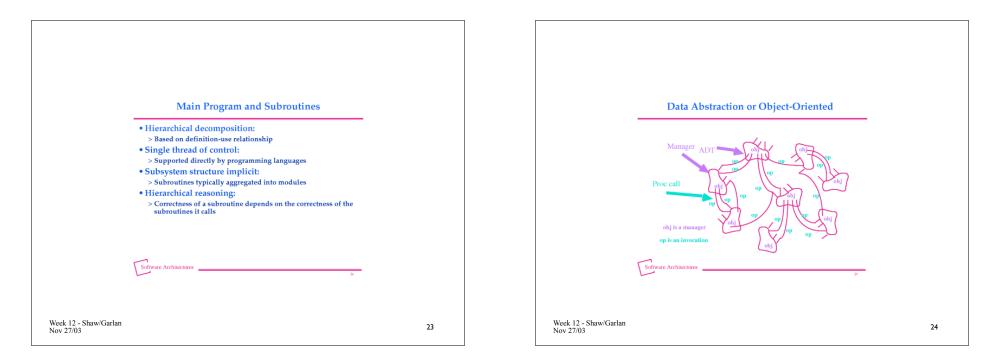


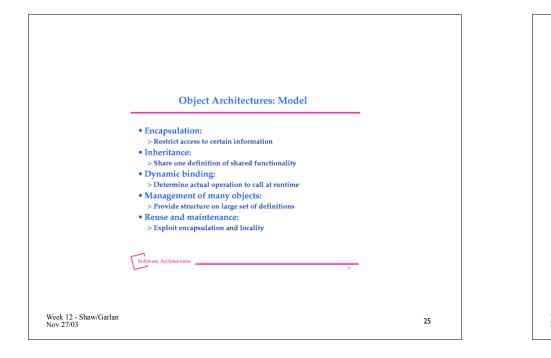


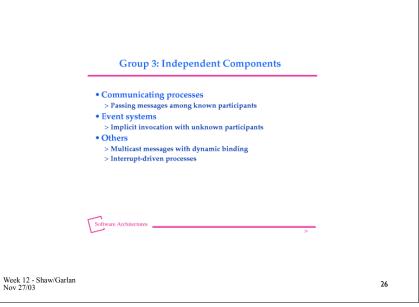


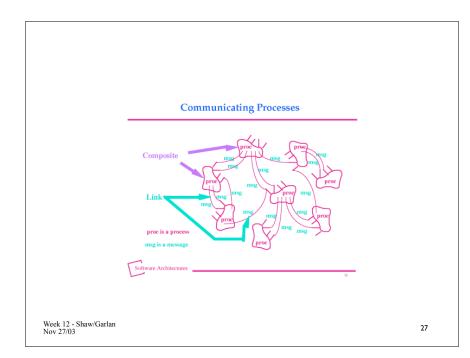


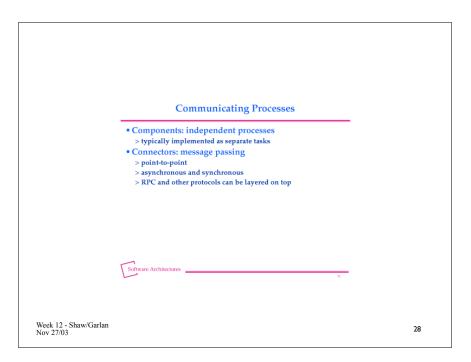


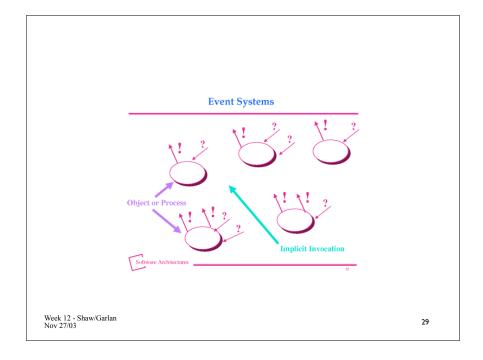


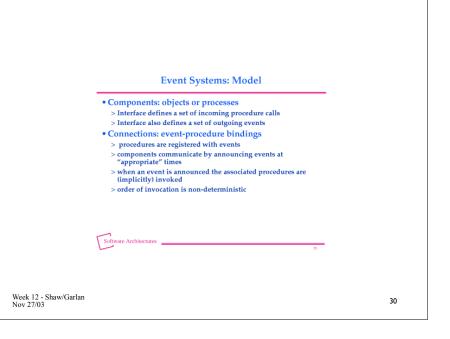




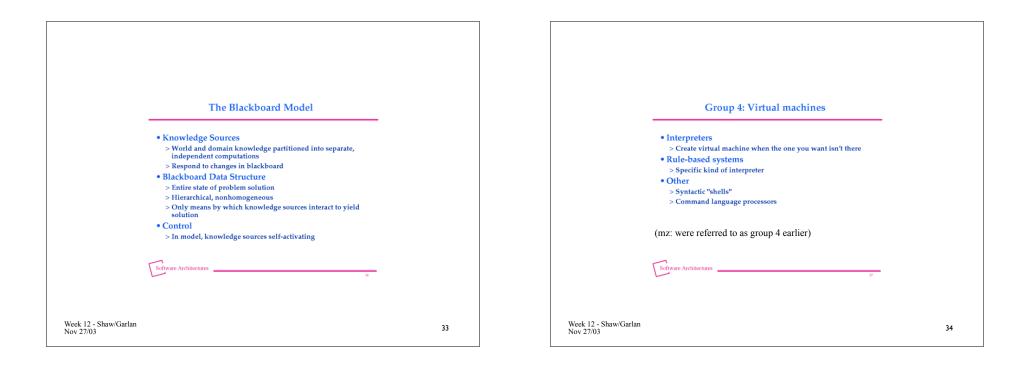


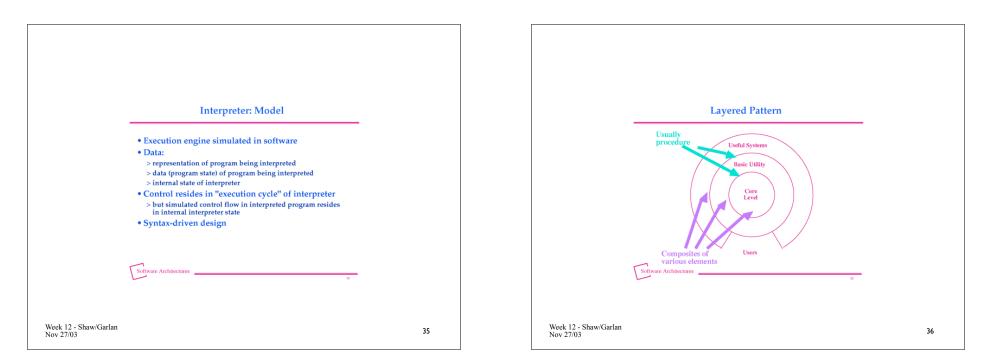


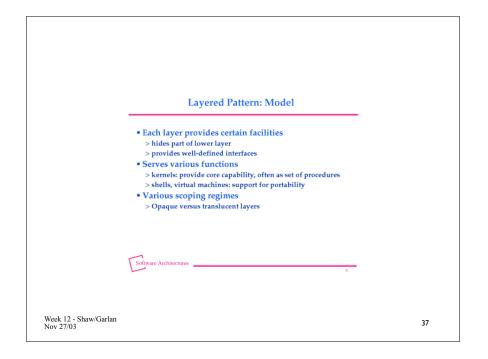












	Com	_				
	System Model Pipeline	Components	Connections	Control Struct		
	stream ->	filters (local processing)	data flow ASCII streams	data flow		
	Data abstractio	n (object-orio	ented)			
	localized state maint	servers (ADTs, objs)	procedure call	decentralized, single thread		
	Events					
	implicit invocation	independent components	blind announce	loose coupling		
	Interpreter virtual machine	state mach, two memories	fetch, store	input-driven		
	Repository central database Software Architectures	1 memory N processes	direct access or proc call	internal or external		
				e	-	
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