
Formal Validation of Domain-Specific Languages with Derived Features and Well-Formedness Constraints

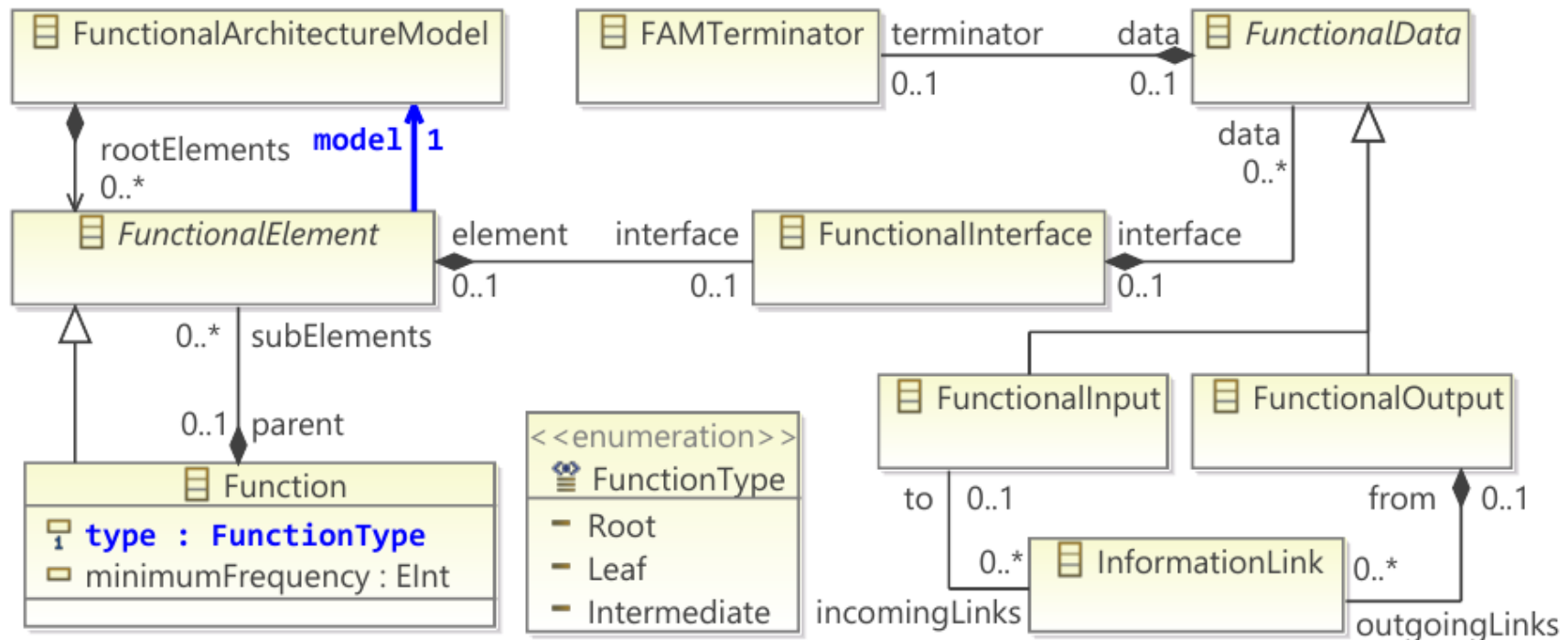
Presenter: Nick
February 05, 2018

Overview

- Motivation
- DSL Validation Tool
- DSL Validation Workflow
- Experimental Results
- Conclusions

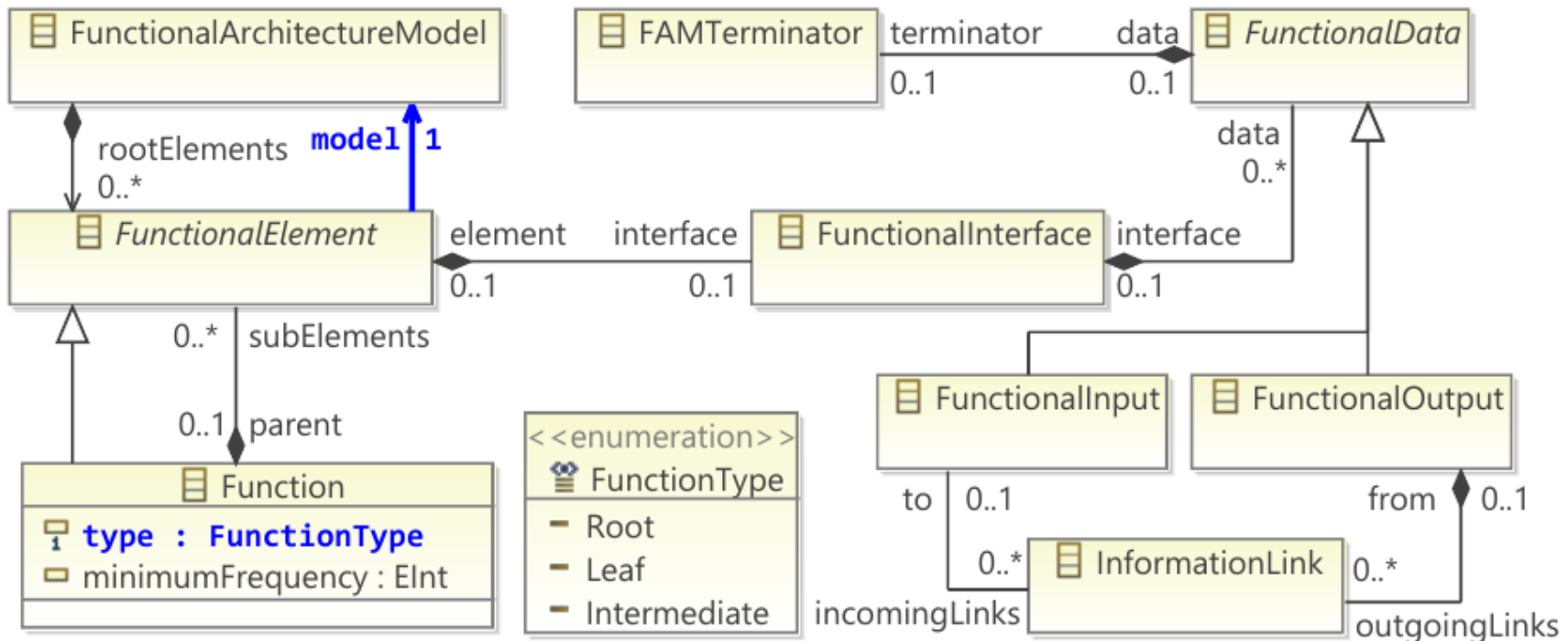
Domain Specific Languages (DSLs)

- Components of a DSL
 - Metamodel
 - Derived features
 - Well-formedness constraints



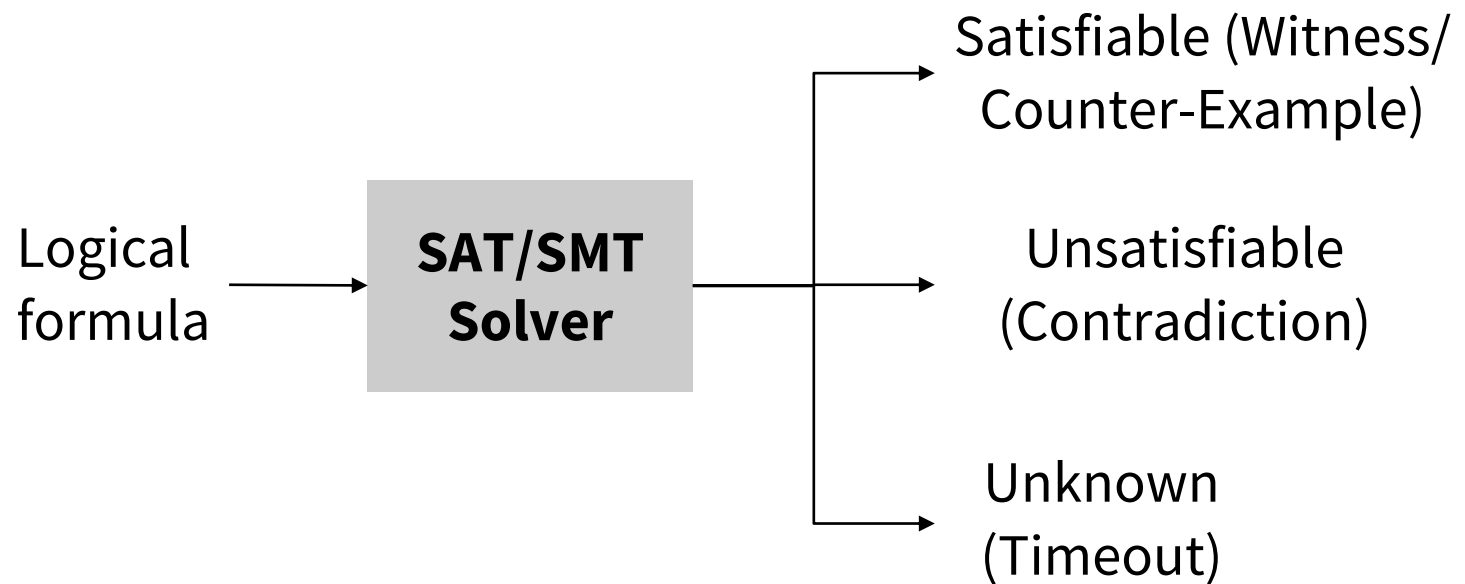
Domain Specific Languages (DSLs)

- Validation Challenges
 - Complex metamodel and constraints
 - Infinite range of models

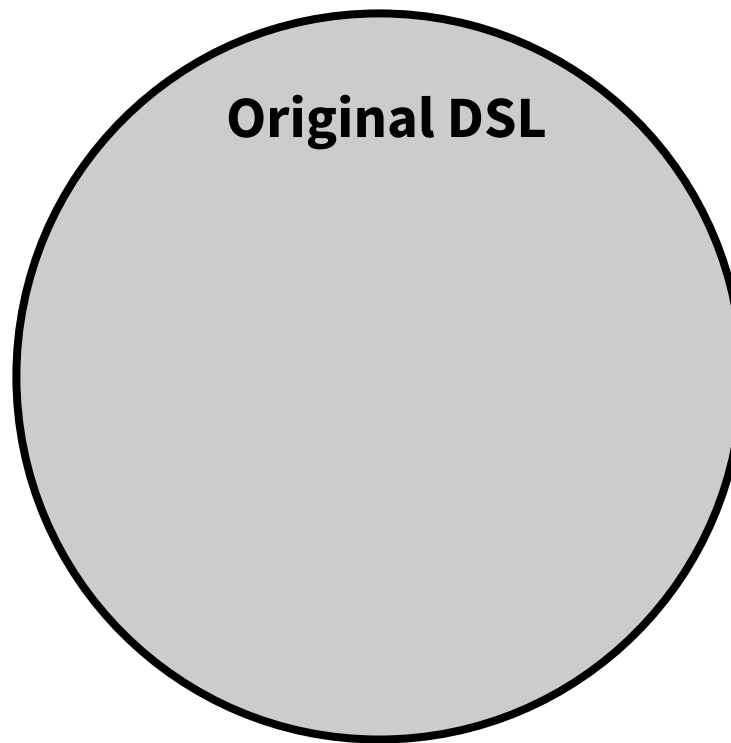


SAT/SMT Solvers

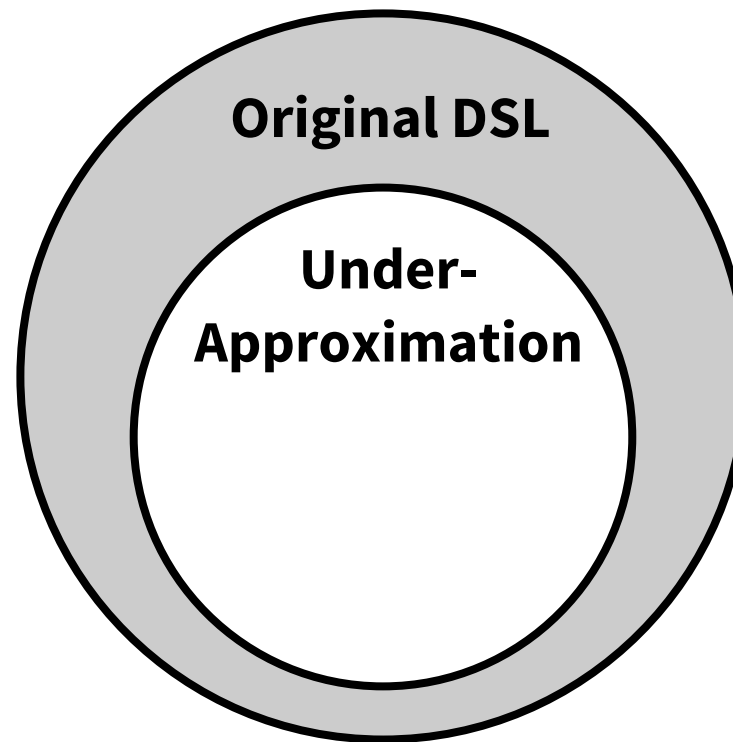
- Properties
 - Checks satisfiability of a logical claim.
 - SMT is more expressive than SAT



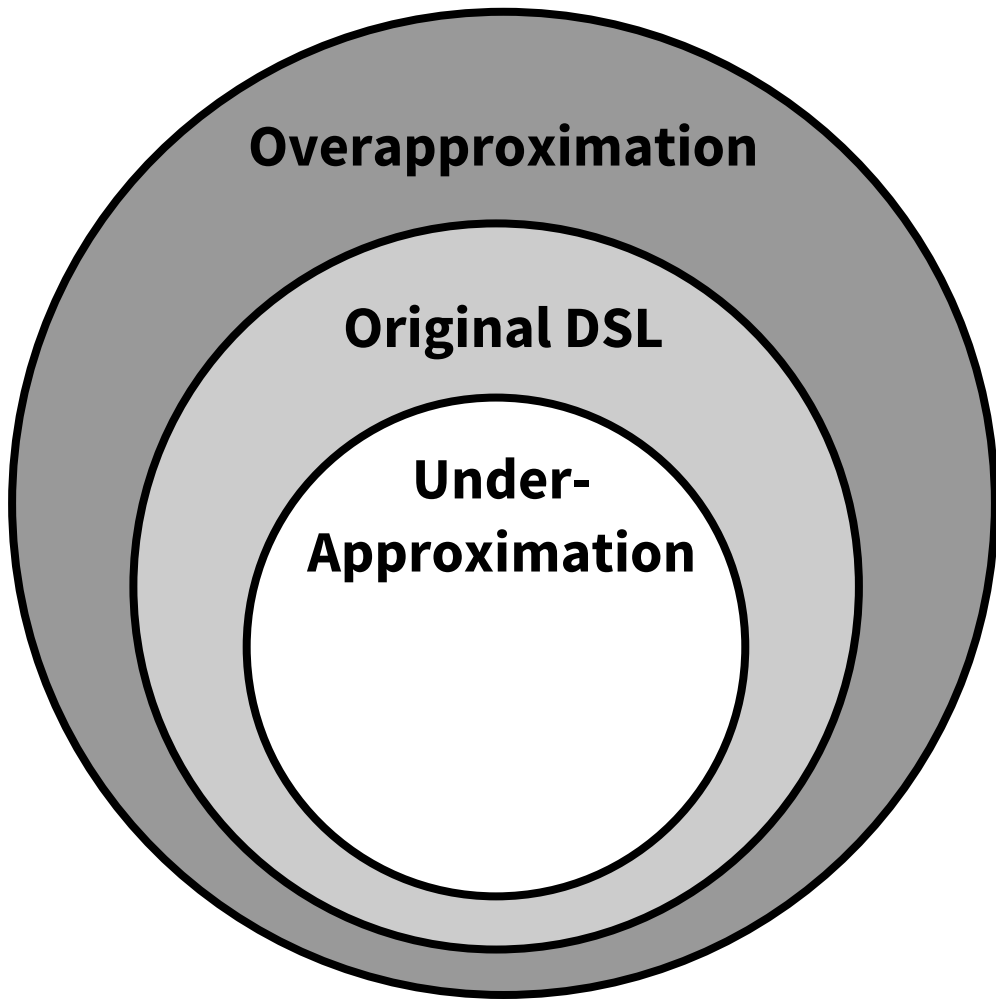
First-Order Logic (FOL) Approximation



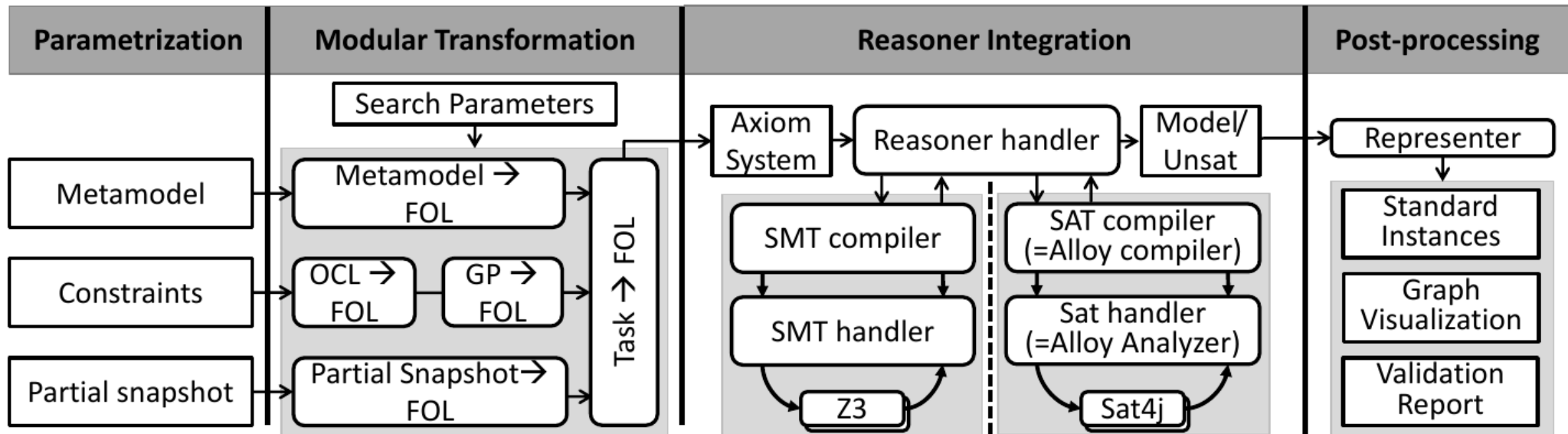
First-Order Logic (FOL) Approximation



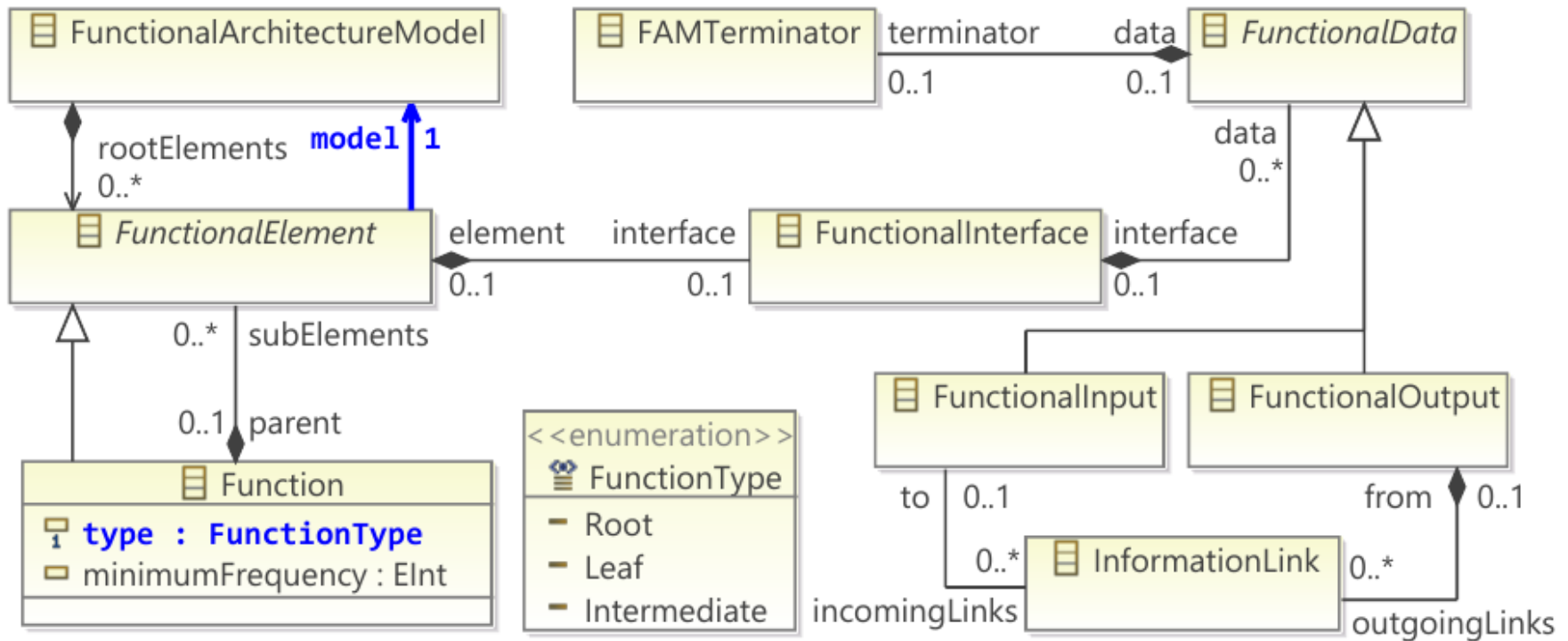
First-Order Logic (FOL) Approximation



DSL Validation Tool

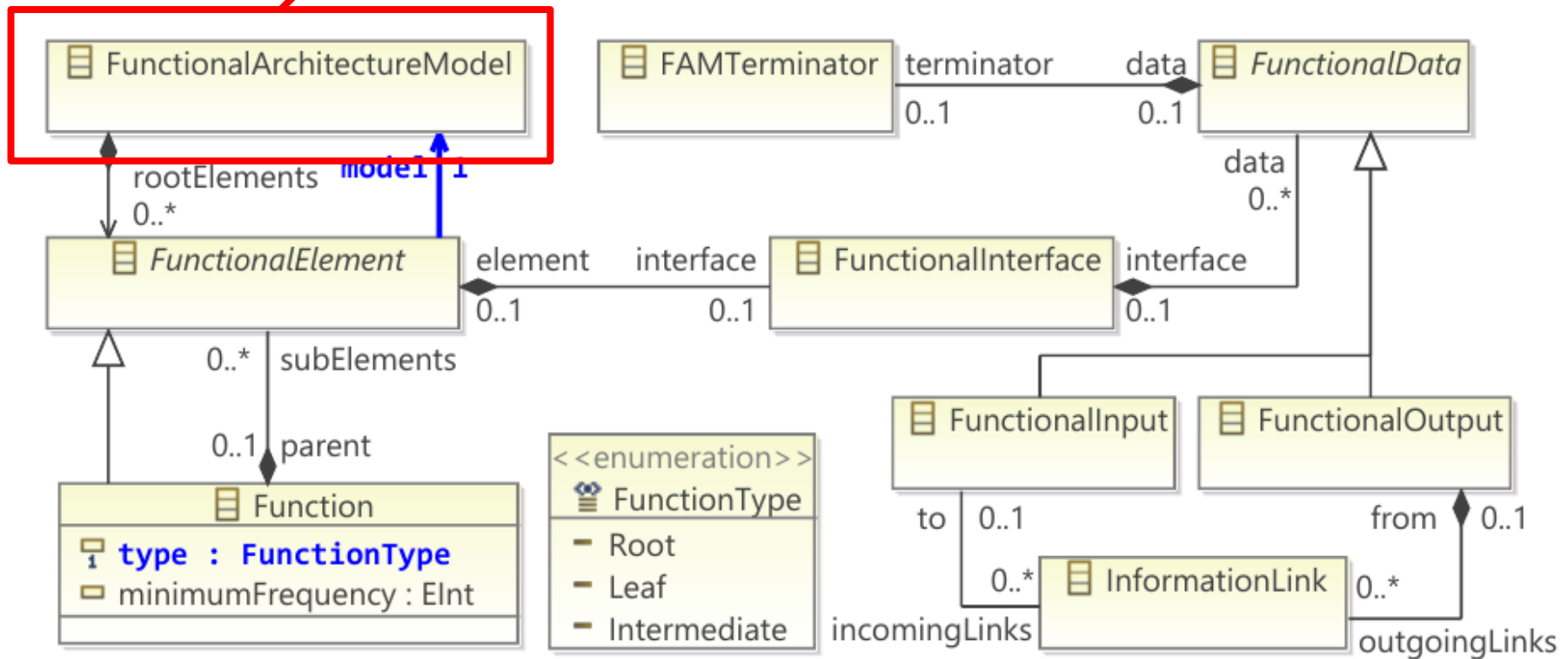


Formalisation of the Metamodel

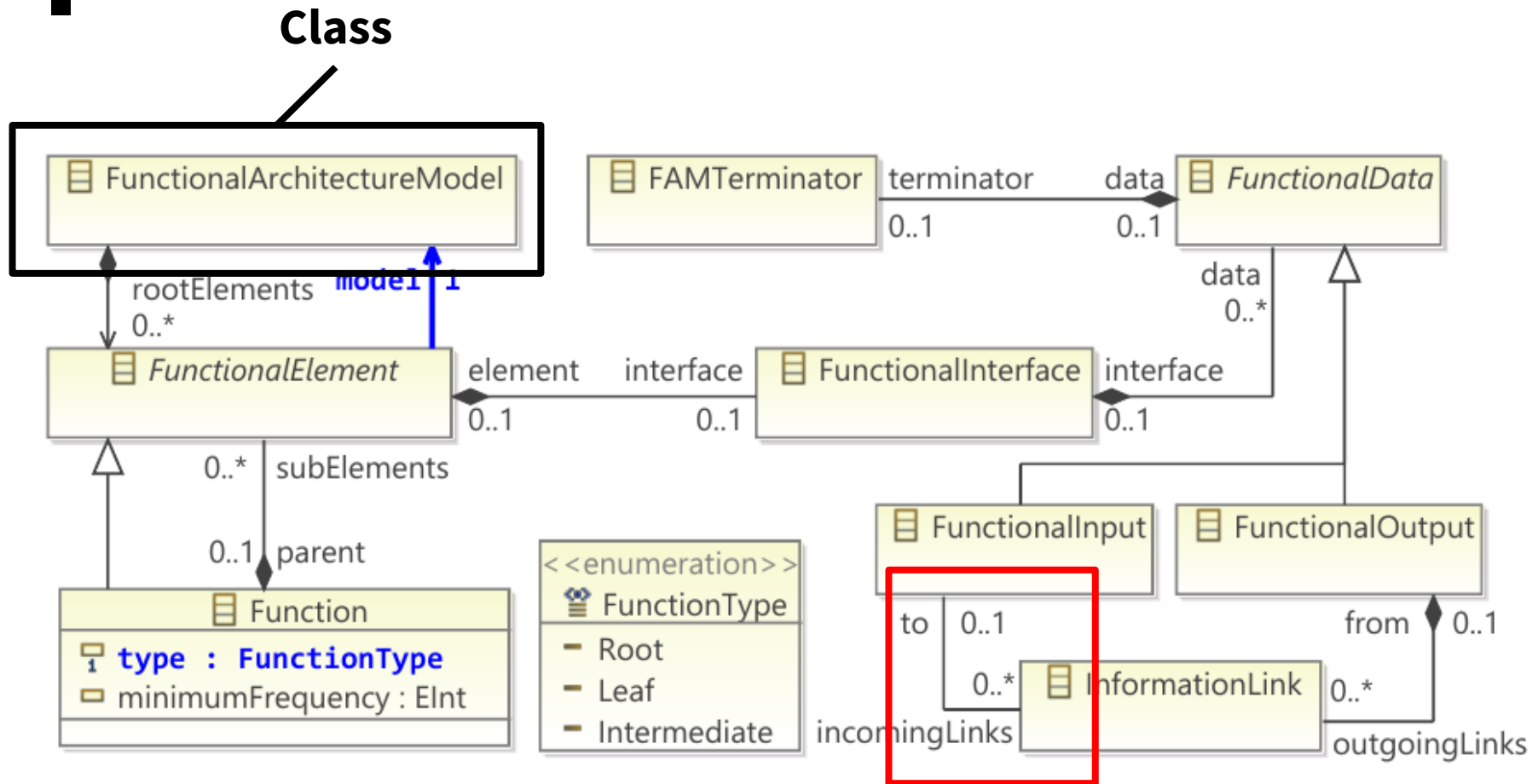


Formalisation of the Metamodel

Class

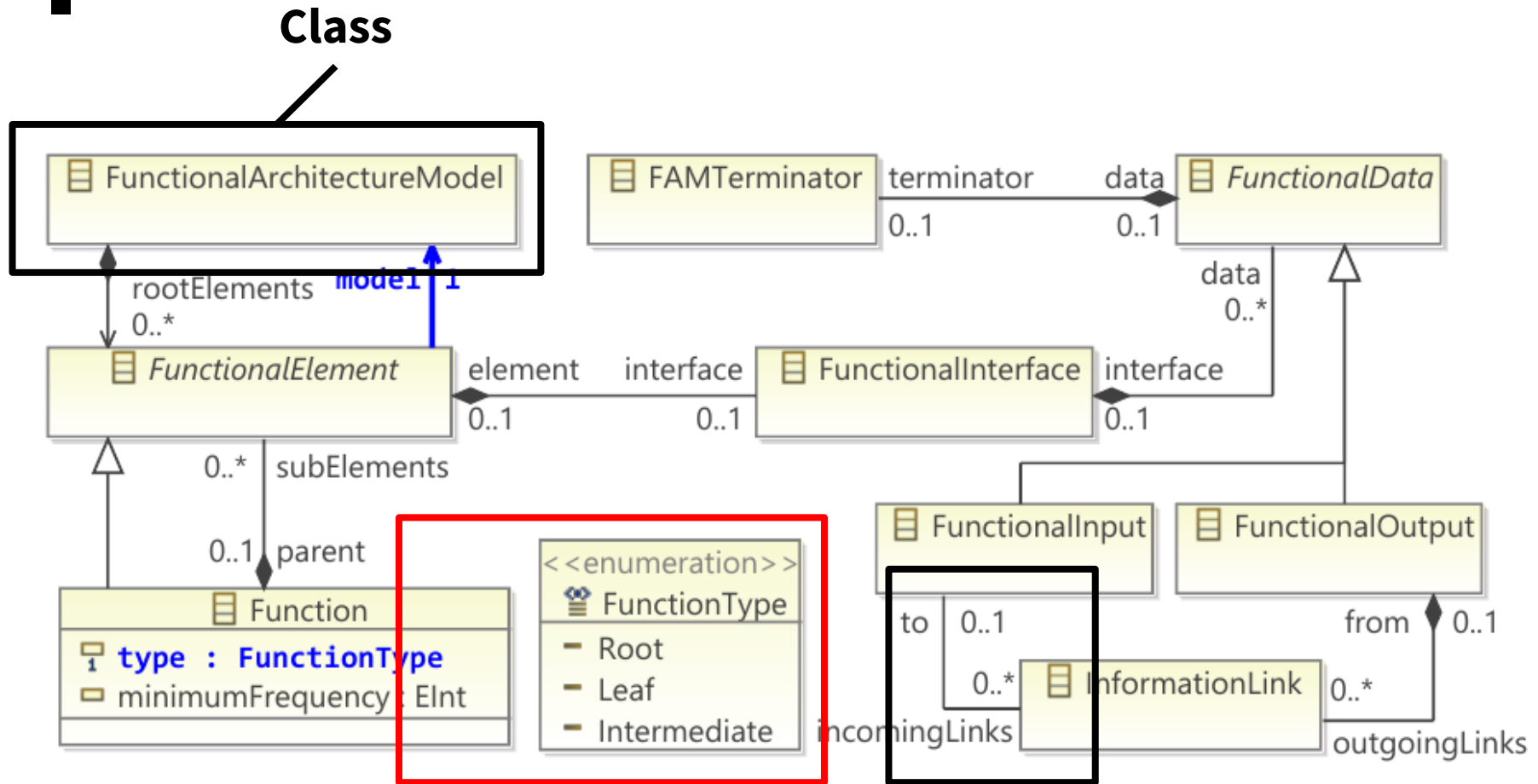


Formalisation of the Metamodel



**Relationships, Multiplicity
& Inverse Edges**

Formalisation of the Metamodel

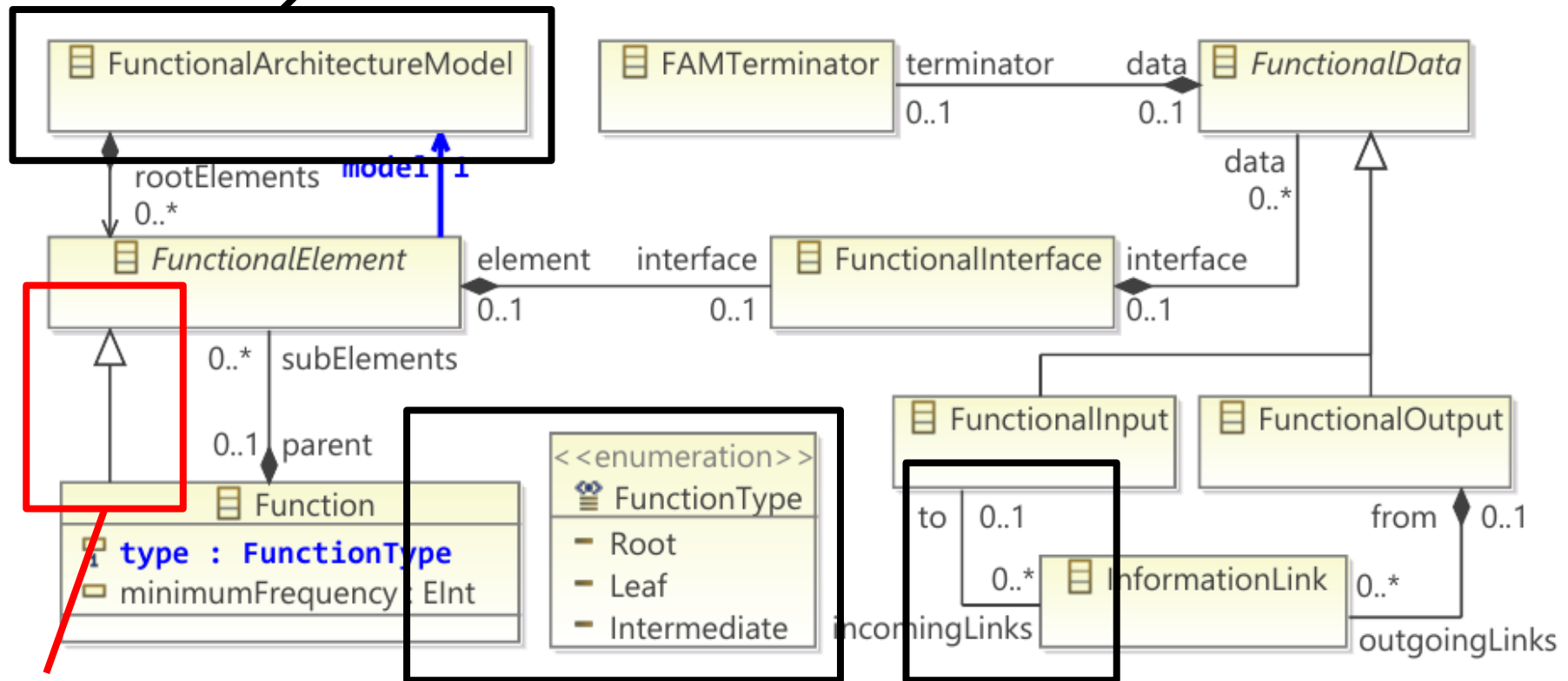


**Enumerations
& Attributes**

**Relationships, Multiplicity
& Inverse Edges**

Formalisation of the Metamodel

Class



Type Hierarchy

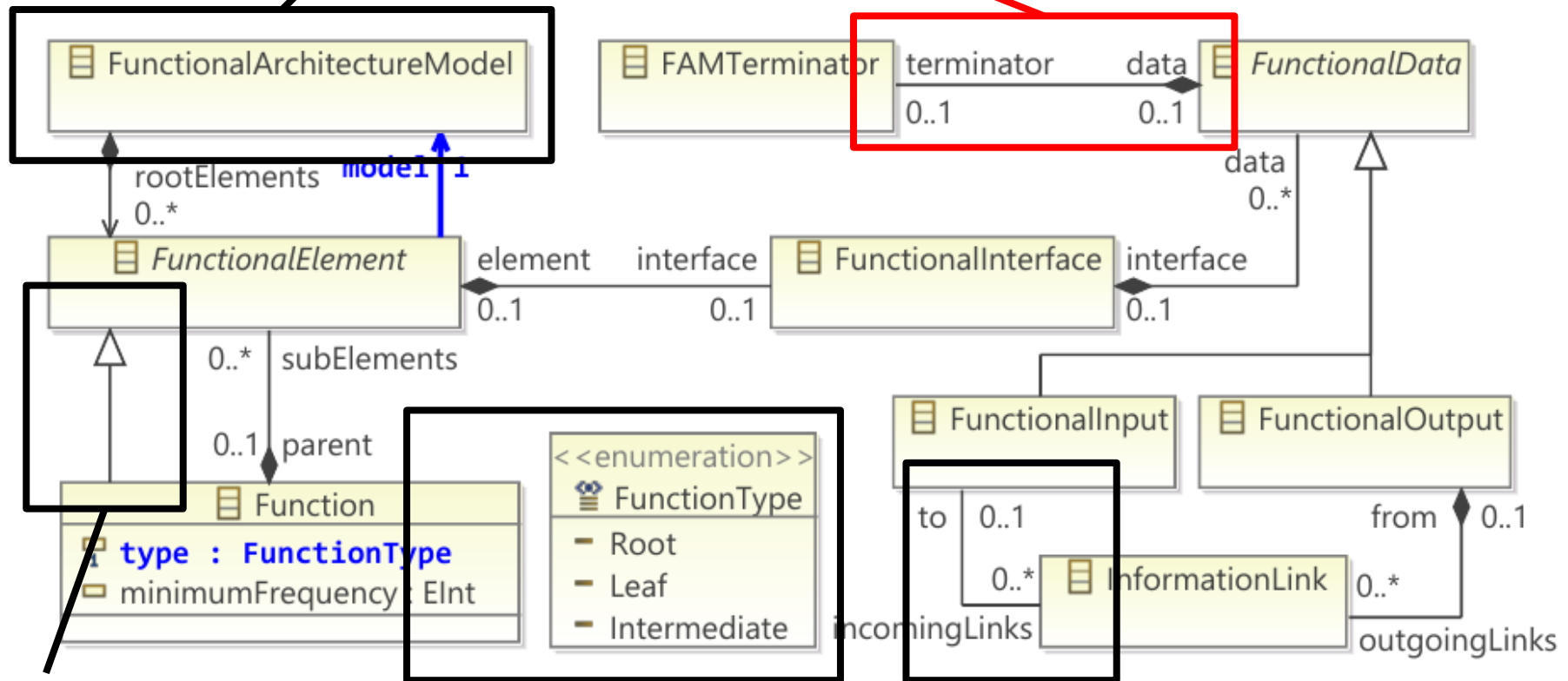
Enumerations & Attributes

Relationships, Multiplicity & Inverse Edges

Formalisation of the Metamodel

Class

Containment



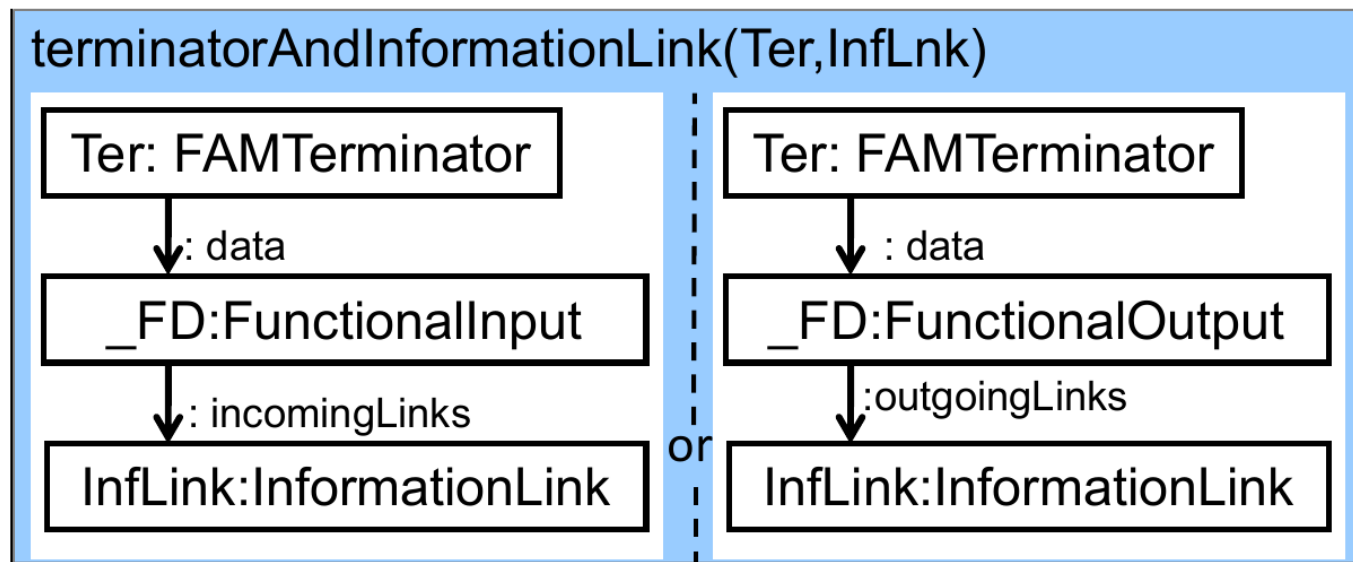
Type Hierarchy

Enumerations & Attributes

Relationships, Multiplicity & Inverse Edges

Formalisation of Constraints

- Supported Constraints
 - Classifier
 - Path
 - Equality
 - Pattern call
 - Check



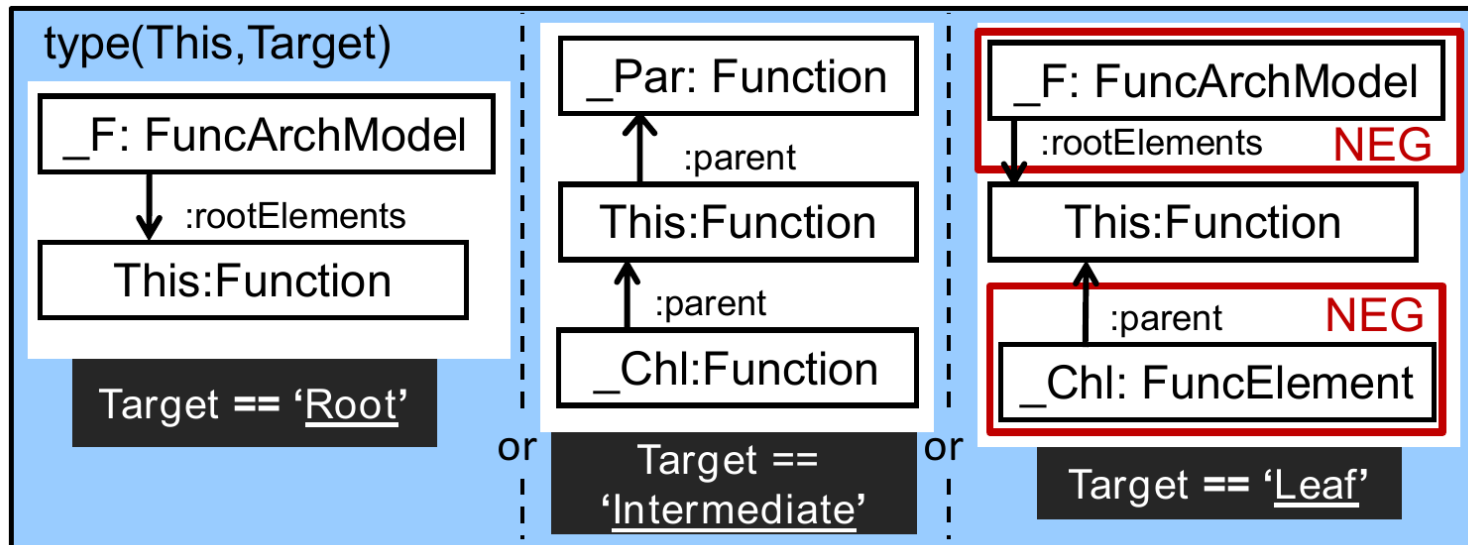
Formalisation of Constraints

- Supported Constraints

- Classifier
- Path
- Equality
- Pattern call
- Check

- Usage

- Well-formed constraints
- Derived patterns



Partial Snapshots

- Relaxed Constraints
 - Undefined attributes
 - Abstract objects
 - Unconnected partitions
 - Missing/extra edges
 - Removed objects

<code>r1: Function</code>
<code>type = ::Root</code> <code>minimumFrequency = ?</code>

<code>r2: Function</code>
<code>type = ::Root</code> <code>minimumFrequency = ?</code>

<code>i1: Function</code>
<code>type = ::Intermediate</code> <code>minimumFrequency = ?</code>

<code>i2: Function</code>
<code>type = ::Intermediate</code> <code>minimumFrequency = ?</code>

<code>l1: Function</code>
<code>type = ::Leaf</code> <code>minimumFrequency = ?</code>

<code>l2: Function</code>
<code>type = ::Leaf</code> <code>minimumFrequency = ?</code>

Partial Snapshots

- **Relaxed Constraints**

- Undefined attributes
- Abstract objects
- Unconnected partitions
- Missing/extra edges
- Removed objects

- **Semantic Modifiers**

- Positive/Negative
- Injective/Shareable
- Modifiable/Unmodifiable

<code>r1: Function</code>
<code>type = ::Root</code> <code>minimumFrequency = ?</code>

<code>r2: Function</code>
<code>type = ::Root</code> <code>minimumFrequency = ?</code>

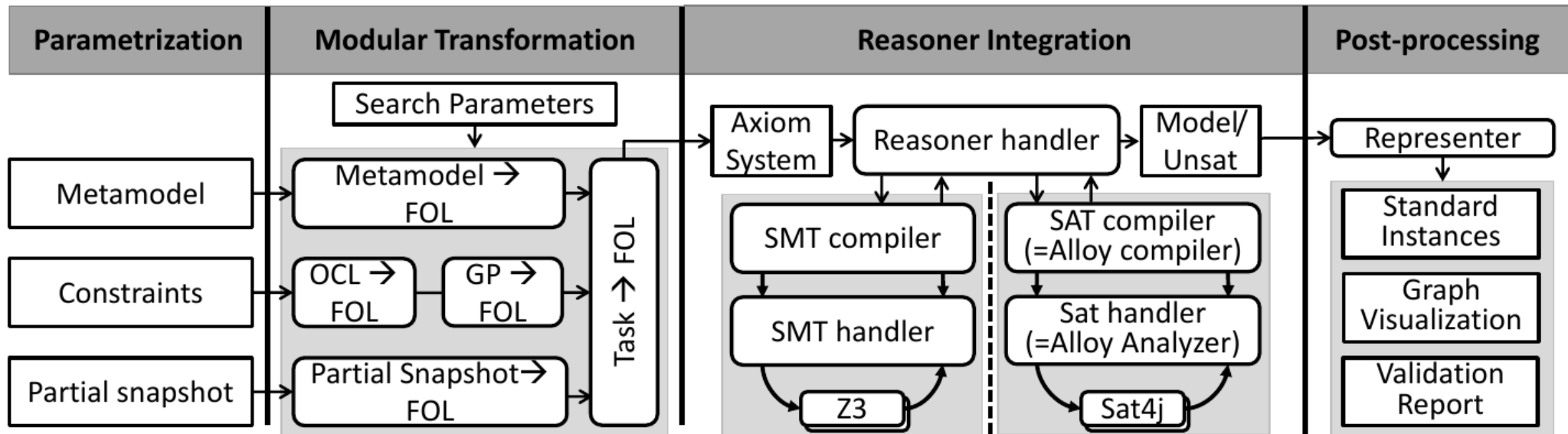
<code>i1: Function</code>
<code>type = ::Intermediate</code> <code>minimumFrequency = ?</code>

<code>i2: Function</code>
<code>type = ::Intermediate</code> <code>minimumFrequency = ?</code>

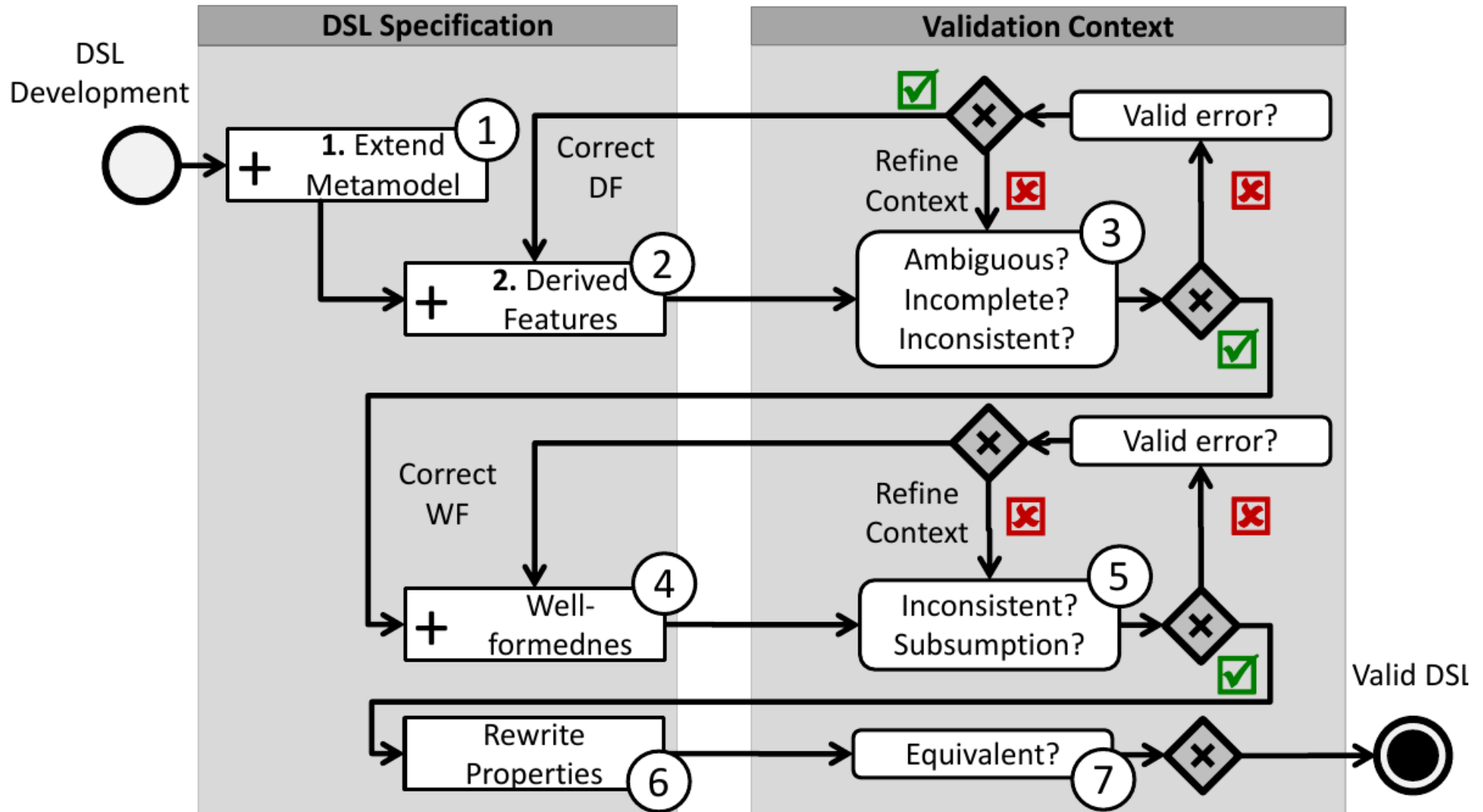
<code>l1: Function</code>
<code>type = ::Leaf</code> <code>minimumFrequency = ?</code>

<code>l2: Function</code>
<code>type = ::Leaf</code> <code>minimumFrequency = ?</code>

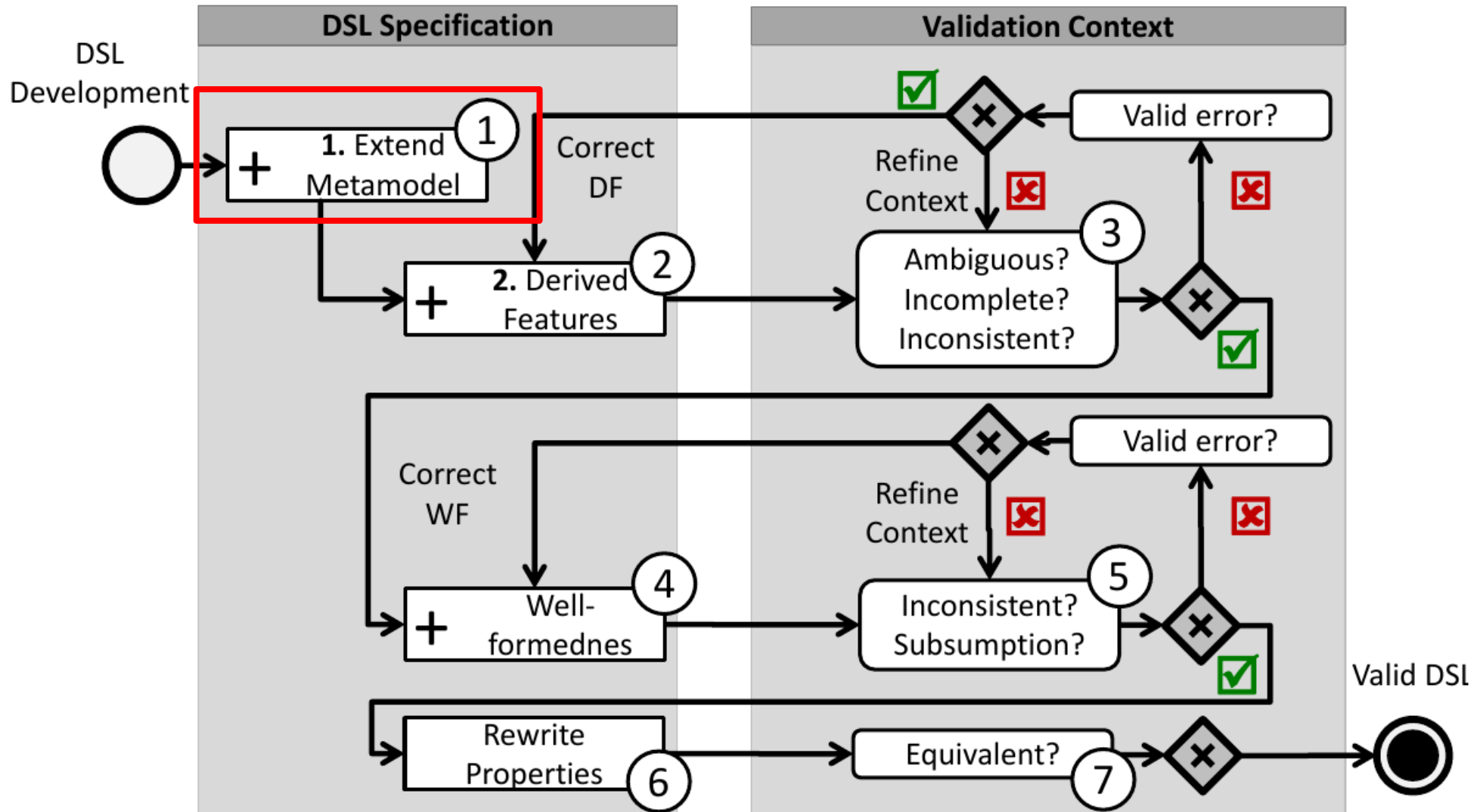
DSL Validation Tool



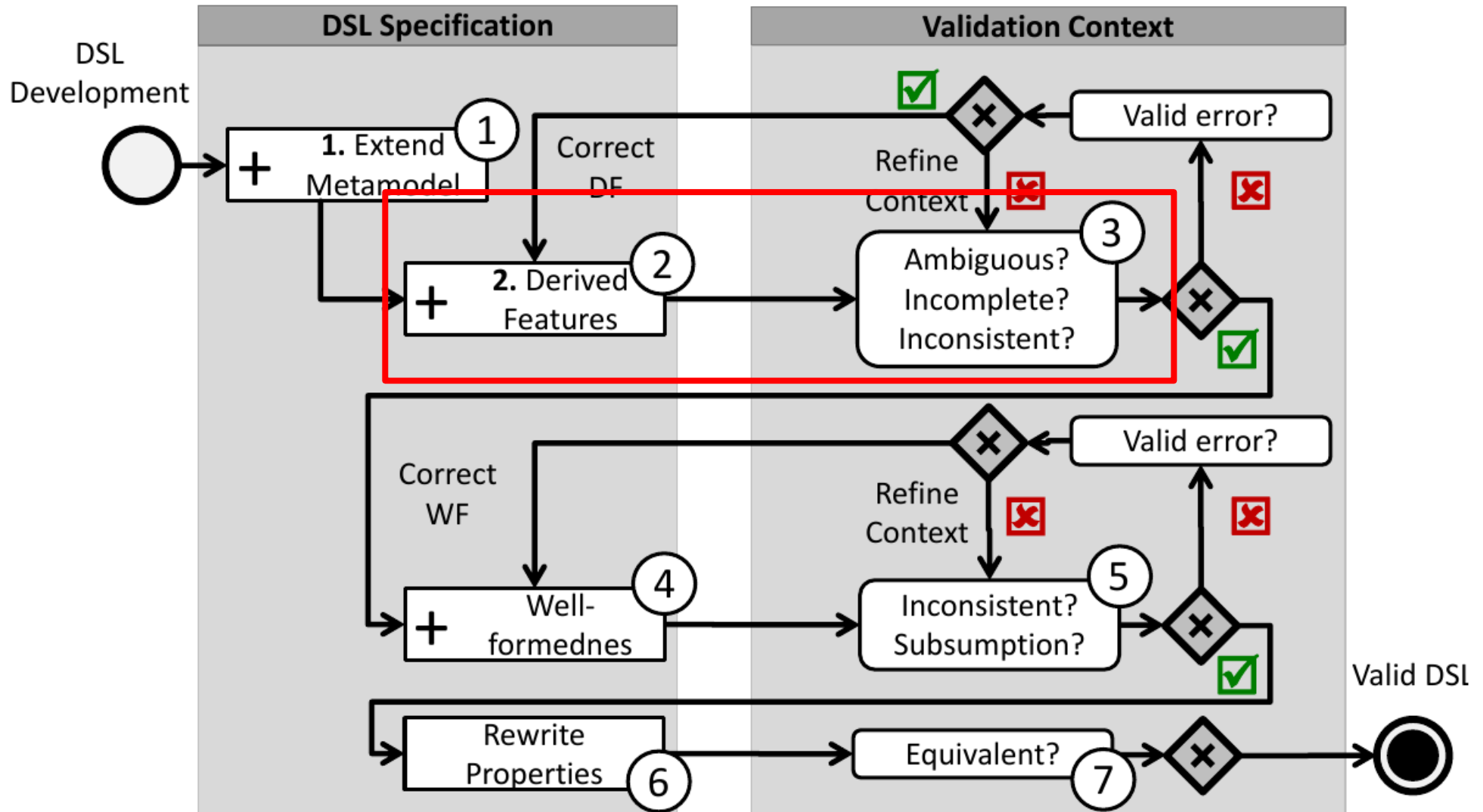
DSL Validation Workflow



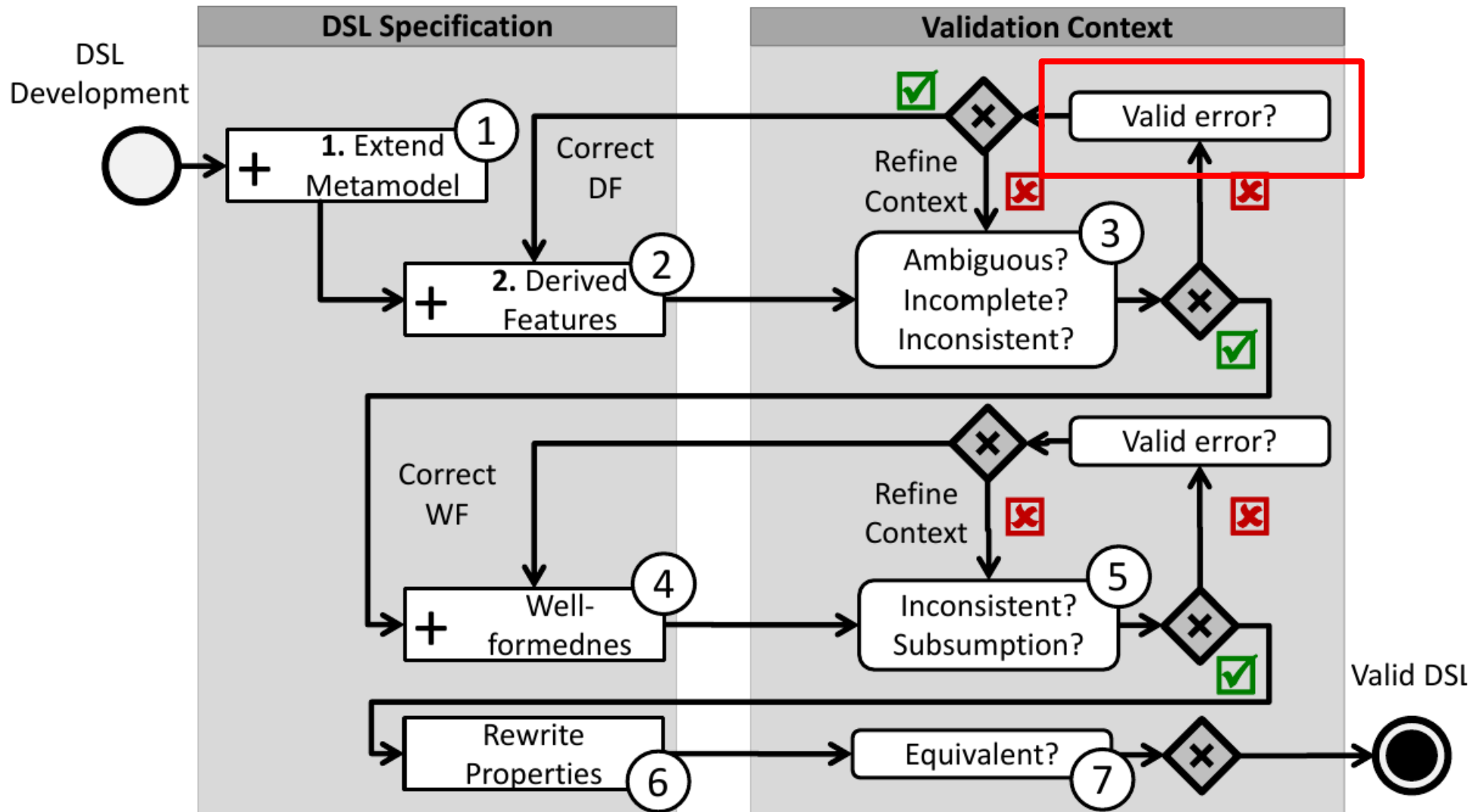
DSL Validation Workflow



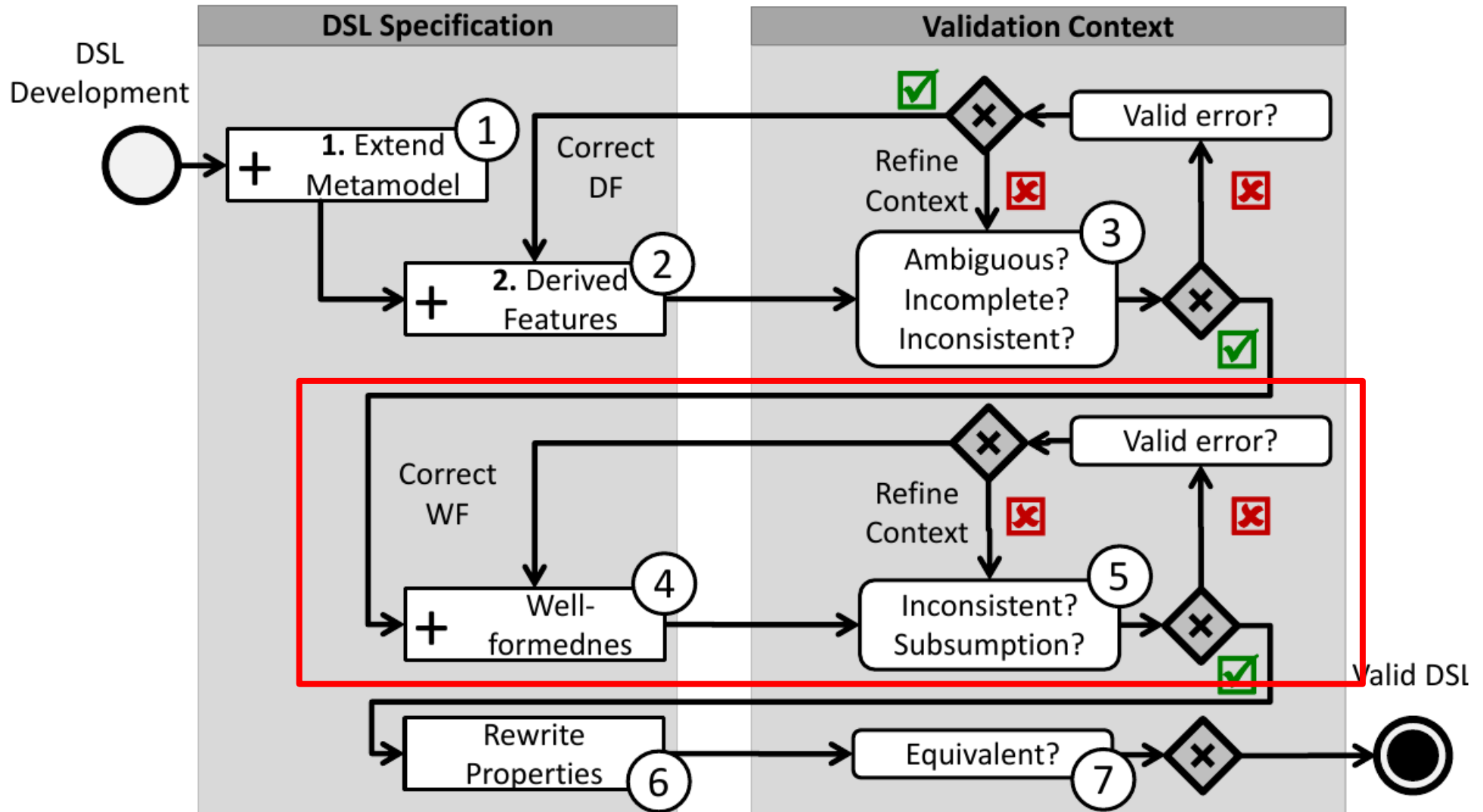
DSL Validation Workflow



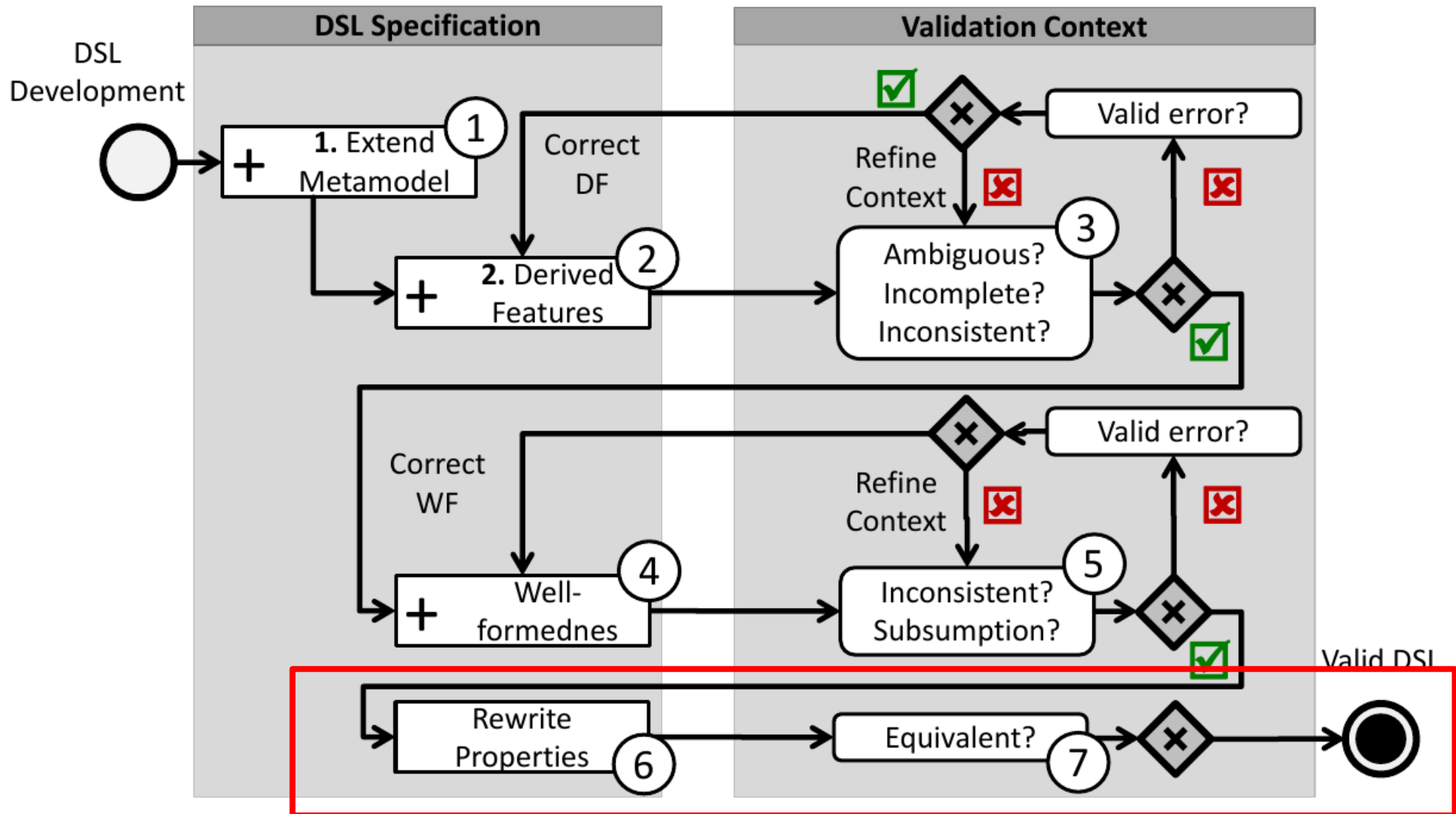
DSL Validation Workflow



DSL Validation Workflow



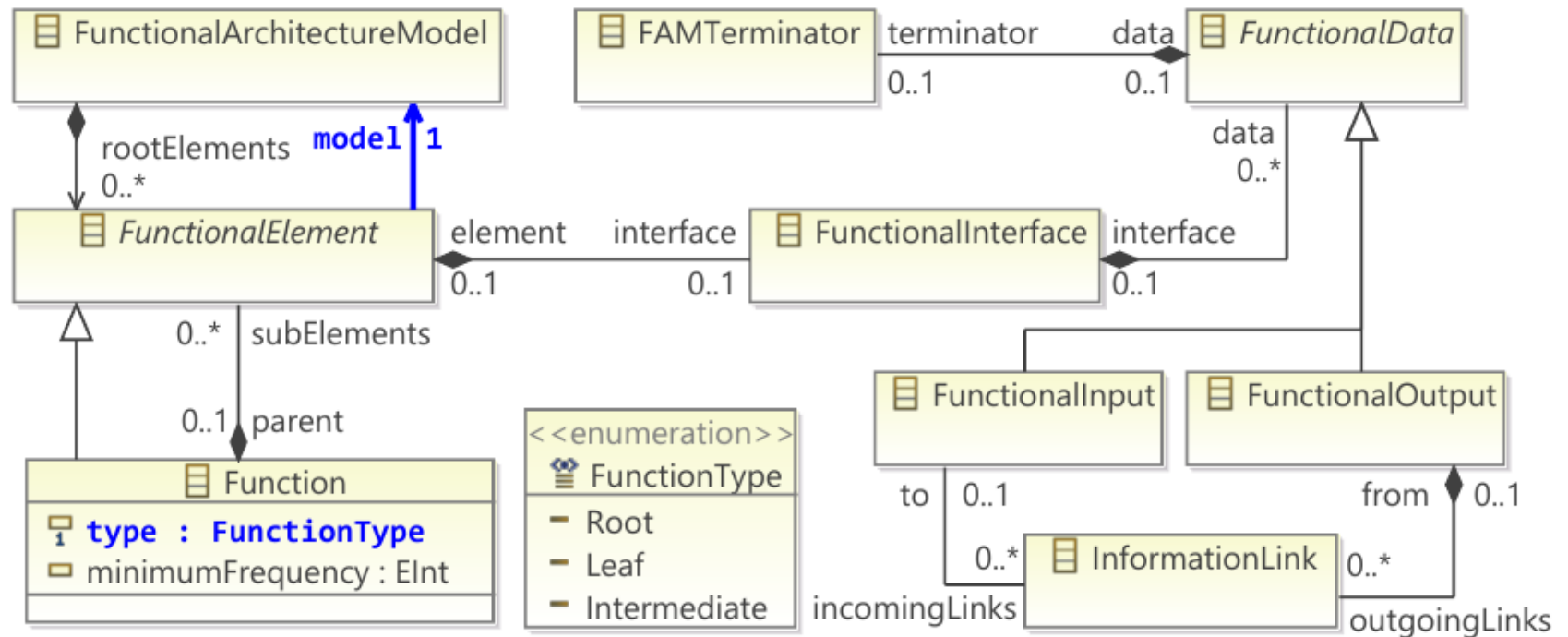
DSL Validation Workflow



Runtime Measurements

- Preliminary Results

- Z3 (SMT) generally outperforms Alloy (SAT)
- Alloy outperforms Z3 in model structure generation



The End

- **Problem**
 - Validation of complex DSLs
- **Approach**
 - Approximate DSL in first-order logic
 - Check for satisfiability of resulting formula
 - Convert witness/counterexample into model
- **Discussion Points**
 - How well does the approach scale?
 - Is it applicable to validating UML?
 - What are “useful” constraints for a DSL?
 - Is it applicable to synthesising constraints?