

Taming TSO Memory Consistency with Models

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Plan of Attack

- 1. High Level Motivation and Overview
- 2. (Some) Details of TSO Consistency
- 3. A look at TSO Helper via an Example
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- 5. Future Directions

1. High Level Motivation and Overview

The End of the Age of Sequential Performance

Transistor Count Clock Speed Single Core Performance

Sequential algorithms will no longer see the same speedups with new processor generations



The Beginning of the Age of Concurrency



Intel Xeon Phi 50 Core Microprocessor

The Beginning of the Age of Concurreny

- Need multi-threaded algorithms that take advantage of all available CPU cores
- In non-trivial multi-thread algorithms (versus 'embarrassingly' parallel algorithms), communication between threads is required
- Threads communicate by writing and reading messages to and from shared system memory

Sequential Memory Consistency

At the hardware level, the ordering of reads and writes in a program's source code is not always the order that these reads and writes will be executed

Memory Consistency Model

- Defines 'how much' the original order of reads/writes in the source code can be re-ordered
- Sequential Consistency—This is the model we learned in school
 - Reads always return values from the shared memory
 - A write operation is always committed to main memory before the next instruction is executed

E.g.: When I write a value, I know everyone can see it!

TSO Memory Consistency

- TSO Memory Consistency is the native memory consistency model of all modern x86 and x64 CPUs (both Intel and AMD CPUs)
- In a system with TSO Memory Consistency, Read operations executed by a CPU core are not immediately applied to main memory

E.g.: When I write a value, I'm not sure if everyone can see it!

2. (Some) Details of TSO Consistency

TSO Write Buffering

- Under TSO, write operations executed by threads ARE NOT immediately committed to memory
- When a thread executes x=v, the CPU takes the target variable x and value v, and puts it into a per-thread FIFO queue called a write buffer
- If you execute a WRITE, followed by a READ, the READ may grab a value from shared memory BEFORE the write is executed!



TSO Write Buffering

- At some later point in time, the buffered writes are committed to memory in the same order that they entered the write buffer
- It is ONLY after a write is committed that other processes can see the new value of the variable that was written to!



Delayed Writes Can be Dangerous



Crossing the Street

0 0

This person has made a **decision** to cross the road based on the incorrect **assumption** that the change he made to the state of the stop light is **visible to all others**



3. A look at TSO Helper via an Example

A Banking Application

- X and Y are ATMs that perform deposit updates on a single bank account
- The current value of the account is stored in the shared variable acct
- If it is a joint account, we must ensure only one account owner can deposit their money into the account at a time

ATM X :	ATM Y :
1. X_OTHER_BANKING_WORK();	8. Y_OTHER_BANKING_WORK();
2. X_UPDATE = true	9. Y_UPDATE = true
3. await(¬YUPDATE)	10. await(¬X_UPDATE)
4. x_temp = acct + 1	11. y_temp = acct + 1
5. acct = x_temp	12. acct = y_temp
6. X_UPDATE = false	13. Y_UPDATE = false
7. goto 1	14. goto 8

















- The Banking Algorithm works under SC where writes are immediately committed to shared memory
- It will not (always) work under TSO if writes are buffered

Can TSO Helper Help Us Detect this Issue?

TSO Helper Designer – 'Read/Write' Model



A Simpler Model



Run the Model Through TSO Helper

TCO LIA	Der
	JCI
Access all	TSO Helper tools from this control panel
Enter setungs	
Input file	SimpleBankingWithBarrier.tso Set
Output file	SimpleBankingWithBarrier_out.tso Set
Models path	ect\runtime-EclipseApplication\TestDiag30\model Set
Tools	
Tools Run	Replace Function nodes with Read/Write nodes
Tools Run Run	Replace Function nodes with Read/Write nodes Expand graph into Tree representing 1 iteration(s).
Tools Run Run Run	Replace Function nodes with Read/Write nodes Expand graph into Tree representing 1 iteration(s). Transform graph using 1 iteration(s) and a commit filter of 1

TSO Helper Does Not Like The Algo

 After exploring three iterations of the algorithm, TSO Helper concludes (at least within 3 iterations) that their is NO point in the algorithm at which any write can be guaranteed to have been committed.

But this should be obvious!

From inspection of the algorithm, it is clear that—given an infinite sized write buffer—that the write buffer could theoretically fill up forever.

What if we add a Memory Barrier?

Banking With a Barrier

- A memory barrier instruction dequeues and commits all buffered writes
- It can be a very expensive instruction
- Now we are ensured that X's write, X_UPDATE = true, is visible to Y when X makes its next read.





4. Topics not covered

Topics not covered

- How TSO Helper works!
 - The key 'TSO Helper' lemma
- How TSO Helper can use knowledge about interprocess synchronization to better infer where writes must have been committed
- TSO Helper's ability to filter displayed commits by the iteration they were generated in

5. Future Directions

Future Directions

- Algorithms to better layout transformed graphs
- Use of MMTF to connect 'Read/Write' graphs for different processes
- Case Study: Is TSO Helper practical and useful?

Thanks!