Promela/SPIN

Acknowledgements:

These notes used some of the material presented by Flavio Lerda as part of Ed Clarke's model-checking course

SPIN

- System invariants, general LTL properties
- Construction of the system's execution
- Proof approximation"

Explicit State Model Checker

CRepresents the system as a finite state machine

- Visits each reachable state (state space) explicitly (using Nested DFS)
- ⇒ Performs on-the-fly computation
- Uses partial order reduction
- ⇒ Efficient memory usage
 - State compression
 - Sit-state hashing

⊃ Version 4:

SUninterpreted C code can be used as part of Promela model

3



1

Promela (Process Meta Language)

- Asynchronous composition of independent processes
- Communication using channels and global variables
- Non-deterministic choices and interleavings
- Based on Dijkstra's guarded command language

```
Every statement guarded by a condition and blocks until condition
becomes true
Example:
```

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```
while (a != b)
    skip /* wait for a == b */
vs
    (a == b)
```

Process Types

- State of variable or message channel can only be changed or inspected by processes (defined using proctype)
- ; and -> are statement separators with same semantics.

-> used informally to indicate causal relation between statements Example:

```
byte state = 2;
proctype A()
{ (state == 1) -> state = 3
}
proctype B()
{ state = state -1
}
```

state here is a global variable







Other constructs	
≎ Do loops	
do	
:: count = count + 1;	
:: $count = count - 1;$:: $(count == 0) \rightarrow break$	
od	
	10







Message Passing		
chan qname = [16] of {short}		
qname!expr - writing (appending) to channel		
gname?expr - reading (from head) of the channel		
qname??expr - "peaking" (without removing content)		
gname!!expr - checking if there is room to write		
can declare channel for exclusive read or write:		
chan in, out; xr in; xs out;		
qname!exp1, exp2, exp3 - writing several vars		
<pre>qname!expr1(expr2, expr3) - type and params</pre>		
qname?vari(var2, var3)		
qname?cons1, var2, cons2 - can send constants		
bLess parameters sent than received – others are undefined		
Solution More parameters sent – remaining values are lost		
Sconstants sent must match with constants received		
14		

Message Passing Example proctype A(chan q1) { chan q2; q1?q2; q2!123 } proctype B(chan qforb) { int x; qforb?x; print("x=%d\n", x) } init { chan qname = [1] of $\{chan\};$ chan qforb = [1] of {int }; run A(gname); run B(qforb); qname!qforb } Prints: 123 15

Randez-vous Communications	
⇒ Buffer of size 0 – can pass but not store messages	
Solution Synchronous Message interactions by definition synchronous	
Example:	
#define msgtype 33	
<pre>chan name = [0] of { byte, byte };</pre>	
proctype A()	
<pre>{ name!msgtype(123);</pre>	
<pre>name!msgtype(121); /* non-executable */</pre>	
}	
proctype B()	
{ byte state;	
name?msgtype(state)	
}	
init	
{ atomic { run A(); run B() }	
1 1	16

Randez-Vous Communications (Cont'd)

If channel name has zero buffer capacity:

- Handshake on message msgtype and transfer of value 123 to variable state.
- Stepse the second statement will not be executable since no matching receive operation in B

If channel name has size 1:

- 𝔅 B can retrieve this message and complete.
- Shen A completes, leaving the last message in the channel

⇒ If channel name has size 2 or more:

SA can finish its execution before B even starts

Example – protocol

17

18

Channels Ain and Bin

- ७ to be filled with token messages of type next and arbitrary values (ASCII chars)...
- by unspecified background processes: the users of the transfer service
- These users can also read received data from the channels Aout and Bout
- The channels are initialized in a single atomic statement...

Shand started with the dummy err message.

Example Cont'd

mtype = {ack, nak, err, next, accept}; proctype transfer (chan in, out, chin, chout) byte o, I; ş in?next(o); do :: chin?nak(I) -> out!accept(I); chout!ack(o) :: chin?ack(I) -> out!accept(I); in?next(o); chout!ack(o) :: chin?err(I) -> chout!nak(o) od 19

		Example (Cont'd)	
init			
{	chan AtoB	= [1] of { mtype, byte };	
	chan BtoA	= [1] of { mtype, byte };	
	chan Ain	= [2] of { mtype, byte };	
	chan Bin	= [2] of { mtype, byte };	
	chan Aout	= [2] of { mtype, byte };	
	chan Bout	= [2] of { mtype, byte };	
	atomic {		
	run tran	nsfer (Ain, Aout, AtoB, BtoA);	
	run tran	nsfer (Bin, Bout, BtoA, AtoB);	
	}		
	Ain!next(0)	;	
	AtoB!err(0)		20









Verification

Generate, compile and run the verifier ♦ to check for deadlocks and other major problems: \$ spin -a mutex \$ cc -0 pan pan.c \$ pan full statespace search for: assertion violations and invalid endstates vector 20 bytes, depth reached 19, errors: 0 79 states, stored 0 states, linked 38 states, matched total: 117 hash conflicts: 4 (resolved) (size s^18 states, stack frames: 3/0) unreached code __init (proc 0); reached all 3 states unreached code P (proc 1): reached all 12 states

Mutual Exclusion

25

26

Crifier: Assertion can be violated

⇔Can use -t -p to find out the trace

≻Or use XSpin

Characterization and the second secon

Have another monitor process ran in parallel
 Allows all possible relative timings of the processes
 Elegant way to check validity of system invariant

Mutual Exclusion in SPIN bool turn, flag[2]; byte ncrit; active [2] proctype user() assert(_pid == 0 || __pid == 1); again: flag[_pid] = 1; turn = 1 - _pid; (flag[1 - _pid] == 0 || turn ==_pid); ncrit++; /* critical section */ ncrit--; flag[_pid] = 0; goto again; active proctype monitor() { assert (ncrit == 0 || ncrit == 1) } 27

Finally,

Can specify an LTL formula and run the modelchecker

Example:

#define p count <= 1</pre>

<L claim:[] р

⇒ Note: all variables in LTL claims have to be global!

⇒LTL claim gets translated into NEVER claim and stored either in .ltl file or at the end of model file

Sonly one LTL property can be verified at a time

⇒ Parameters can be set using XSpin

Spepth of search, available memory, etc.









Simulator

Construction Spin can also be used as a simulator

Simulated the Promela program

⇒It is used as a simulator when a counterexample is generated

 $\$ Steps through the trace

⇔The trace itself is not "readable"

≎ Can be used for random and manually guided simulation as well

A few examples

33

≎Alternating Bit Protocol ≎Leader Election

Alternating Bit Protocol • Two processes want to communicate • They want acknowledgement of received messages • Sending window of one message • Each message is identified by one bit • Alternating values of the identifier















Use assertion (line 57) assert (nr_leaders == 1) this is much more efficient! Eventually a leader is elected ><> [] (nr_leaders == 1)

_	
	Verification of Leader Election
1	<pre>#define N 5 /* nr of processes */</pre>
2	<pre>#define I 3 /* node given the smallest number */</pre>
3	<pre>#define L 10 /* size of buffer (>= 2*N) */</pre>
4	
5	<pre>mtype = {one, two, winner}; /* symb. message names */</pre>
6	chan q[N] = [L] of {mtype, byte} /* asynch channel */
7	
8	byte nr_leaders = 0; /* count the number of processes
9	that think they are leader of the ring */
10	proctype node (chan in, out; byte mynumber)
11	<pre>{ bit Active = 1, know_winner = 0;</pre>
12	byte nr, maximum = mynumber, neighbour;
13	
14	<pre>xr in; /* claim exclusive recv access to in */</pre>
15	xs out; /* claims exclusive send access to out */
16	
17	<pre>printf ("MSC: %d\n", mynumber);</pre>
18	out!one(mynumber) /* send msg of type one */
19	one: do
20	:: in?one(nr) -> /* receive msg of type one */ 42

	Verification of Leader Election
21	if
22	:: Active ->
23	if
24	<pre>:: nr != maximum -> out!two(nr); neighbour = nr;</pre>
25	:: else ->
26	/* max is the greatest number */
27	assert (nr == N);
28	<pre>know_winner = 1;</pre>
29	<pre>out!winner(nr);</pre>
30	fi
33	:: else ->
34	out!one(nr)
35	fi
36	
37	:: in?two(nr) ->
38	if
39	:: Active ->
40	if
	43

	Verification of Leader Election
41	:: neighbour > nr && neighbour > maximum
42	<pre>maximum = neighbour;</pre>
43	out!one(neighbour)
44	:: else ->
45	Active = 0
46	fi
47	:: else -> out!two (nr)
48	fi
49	:: in?winner(nr) ->
50	if
51	<pre>:: nr != mynumber -> printf ("LOST\n");</pre>
52	:: else ->
53	<pre>printf ("Leader \n");</pre>
54	<pre>nr_leaders++;</pre>
55	<pre>assert(nr_leaders == 1);</pre>
56	fi
57	if
58	:: know_winner
59	:: else ->
60	out!winner(nr)
1	44

	Verification of Leader Election			
62	fi;			
63	break			
64	od			
65	}			
66				
67	<pre>init {</pre>			
68	byte proc;			
69	atomic { /* activate N copies of proc template */			
70	<pre>proc = 1;</pre>			
71	do			
72	:: proc <= N ->			
73	<pre>run node (q[proc-1], q[proc%N],</pre>			
74	(N+I-proc)% N+1);			
75	proc++			
76	:: proc > N -> break			
77	od			
78	}			
79	}			
		45		

Summary

 Distinction between behavior and requirements on behavior

SWhich are checked for their internal and mutual consistency

- After verification, can refine decisions towards a full system implementation
 - Section Promela is not a full programming language

Can simulate the design before verification starts

Comments Comments DFS does not necessarily find the shortest

- **counterexample** & There might be a very short counterexample but the verification
- might go out of memory
- Solution for the second second

On-The-Fly

47

48

- System is the asynchronous composition of processes
- ⇒ The global transition relation is never build
- For each state the successor states are enumerated using the transition relation of each process

23



⇒ Hash table

Efficient for testing even if the number of elements in it is very big ($\geq 10^6$)

⇒ Reduce memory usage

Scompress each state

CReduce the number of states

Seartial Order Reduction

When a transition is executed only a limited part of the state is modified

















References

http://spinroot.com/

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29