

# Errata for “Measured Capacity of an Ethernet: Myths and Reality”

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We wish to correct several errors in our paper on the performance of Ethernet [1].

1. In section 2.1, it is incorrectly stated that hosts may transmit a minimal length packet every 51.2 microseconds. In fact, minimum interval between packet arrivals is 67.2 microseconds. This period includes 51.2 microseconds for the packet itself, 6.4 microseconds for the preamble, and 9.6 microseconds for the required inter-packet gap. Similarly, the worst-case collision-resolution time is also 67.2 microseconds, not 51.2 microseconds.
2. In section 2.4.6, we summarized a paper by Takagi and Kleinrock [3] that analyzed a model for 1-persistent CSMA. We did not realize that an error in their model had already been pointed out [2] and corrected [4], resulting in much better agreement with simulation and experimental data.
3. In section 3.2 (and in figure 3-1), we described the DELNI hardware used to connect hosts to our test Ethernet as a “multiport repeater.” In fact, the DELNI is a “transceiver multiplexor,” with somewhat different properties. The net effect is that the delays we calculated for our test networks are approximately 0.5 microseconds too low.

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## References

- [1] David R. Boggs, Jeffrey C. Mogul, and Christopher A. Kent. Measured Capacity of an Ethernet: Myths and Reality. In *Proc. SIGCOMM '88 Symposium on Communications Architectures and Protocols*, pages 222-234. Stanford, CA, August, 1988.
- [2] Khosrow Sohraby, Mart L. Molle, and Anastasios N. Venetsanopoulos. Comments on “Throughput Analysis for Persistent CSMA Systems”. *IEEE Transactions On Communications* COM-35(2):240-243, February, 1987.
- [3] Hideaki Takagi and Leonard Kleinrock. Throughput Analysis for Persistent CSMA Systems. *IEEE Transactions On Communications* COM-33(7):627-638, July, 1985.
- [4] Hideaki Takagi and Leonard Kleinrock. Correction to “Throughput Analysis for Persistent CSMA Systems”. *IEEE Transactions On Communications* COM-35(2):243-246, February, 1987.