The Complexity of the Comparator Circuit Value Problem

Stephen Cook

Joint work with Yuval Filmus and Dai Tri Man Lê

Department of Computer Science University of Toronto Canada

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Outline of the talk

Define comparator circuits

- Object of the class of problems reducible to CCV (the comparator circuit value problem)
- Give interesting complete problems for CC
- Introduce universal comparator circuits, with resulting robustness properties of CC.
- Support the conjecture that CC and NC are incomparable using oracle separations.

Comparator Circuits

- Originally invented for sorting, e.g.,
 - Batcher's O(log² n)-depth sorting networks ('68)
 - Ajtai-Komlós-Szemerédi (AKS)
 O(log n)-depth sorting networks ('83)
- Can also be considered as Boolean circuits.





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- Subramanian ['90] Defined CC using log space many-one reducibility
- We introduce universal comparator circuits and use them to show that the two definitions coincide.
- Subramanian showed

$$\mathsf{NL}\subseteq\mathsf{CC}\subseteq\mathsf{P}$$

NL is nondeterministic log space

- $\bullet \ \mathsf{Recall} \ \mathsf{NL} \subseteq \mathsf{CC} \subseteq \mathsf{P}$
- But also NL ⊆ NC ⊆ P where NC (the parallel class) contains the problems solvable by uniform polysize polylog depth Boolean circuit families.
- NC contains all context-free languages, and matrix powering and determinants over \mathbb{Z},\mathbb{Q} etc.

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NC and CC are incomparable. (So in particular CC \subsetneq P.)

Intuitively, we think $CC \subsetneq P$ because each of the two comparator gate outputs in a comparator circuit is limited to fan-out one. (More later...)

Example Complete Problems for CC

- CCV
- Stable Marriage Problem
- Lexicographical first maximal matching
- Telephone connection problem
- Others . . .

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If our conjecture (that NC and CC are incomparable) is correct then none of these complete problems has an efficient parallel algorithm.

Stable Marriage Problem (search version) (Gale-Shapley '62)

- Given *n* men and *n* women together with their preference lists
- Find a stable marriage between men and women, i.e.,
 - a perfect matching
 - Satisfies the stability condition: no two people of the opposite sex like each other more than their current partners
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The Stable Marriage problem has been used to pair medical interns with hospital residencies in the USA.

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Lex-first maximal matching decision problems

- Edge Is a given edge $\{u, v\}$ in the lex-first maximal matching of G?
- Vertex Is a given (top) vertex v in the lex-first maximal matching of G?
- The problems are equivalent.

Reducing vertex lex-first maximal matching to Cev













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Notation

- x, y, z, \ldots denote elements of \mathbb{N} (presented in unary)
- X, Y, Z, ... denote binary strings
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- AC⁰ many-one reducibility $R_1(X) \leq_m^{AC^0} R_2(X)$ iff there exists an AC⁰ function F(X) such that

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• Thus CC is the class of relations $R(\vec{x}, \vec{X})$ that are AC⁰ many-one reducible to CCV.

Function Classes

- Given a class C of relations, we associate a class FC of functions as follows.
- A function F taking strings to strings is in FC iff
 |F(X)| = |X|^{O(1)} (p-bounded)
 The bit graph B_F(i, X) is in C
- Here $B_F(i, X)$ holds iff the *i*th bit of F(X) is 1.

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- But Y = H(X) is the output of another comparator circuit.

So we need a universal comparator circuit, taking Y' as input, to compute G(Y).

Universal comparator circuits [Filmus]

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Operation of the gadget:





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• In order to simulate a single arbitrary comparator in a circuit with m wires we put in m(m-1) gadgets in a row, for the m(m-1) possible comparators.

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- In order to simulate a single arbitrary comparator in a circuit with m wires we put in m(m-1) gadgets in a row, for the m(m-1) possible comparators.
- Simulating *n* comparators requires m(m-1)n gadgets.
- Thus there is an AC^0 function UNIV such that if m, n are arbitrary parameters, then

$$U = \text{UNIV}(m, n) = \langle m', n', U' \rangle$$

is a universal circuit with m' wires and n' gates which simulates all comparator networks with at most m wires and at most n comparators.

$$m' = 2m(m-1)n + m$$

$$n' = 4m(m-1)n$$

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- CC is closed under (many-one) log-space reducibility.
- This is becasue NL \subseteq CC, so FCC includes all log space functions. And FCC is closed under composition.
- If $R(X) \leftrightarrow \operatorname{Cev}(F(X))$, where F is log-space computable, then

$$\chi_R(X) = \chi_{\mathrm{CCV}}(F(X))$$

where χ_R is the characteristic function of R.

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- Proof of direction ⇒: This is clear if R(X) is in AC⁰. (An AC⁰ circuit converts into a polysize tree circuit, which converts to a comparator circuit.)
- If $R(X) \in CC$, then

 $R(X) \leftrightarrow \operatorname{Ccv}(F(X))$

for some AC^0 function F(X). Apply a universal circuit to the output of F(X).

The circuit C_k computing R(X) for |X| = k



Conjecture: NC and CC are incomparable

 \bullet Lex-First Max Matching (LFMM) is in CC.

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 The function A → Aⁿ (where A is an n × n integer matrix) is in NC², but we do not know how to put it in CC.

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- Flipping an input to a gate generates a unique flip-path in the circuit from that gate to some output of the circuit.
- But flipping an input to an NC²-gate can generate many parallel flip-paths.

- The oracle $\alpha: \{0,1\}^* \to \{0,1\}^*$ is length preserving.
- $\alpha_n : \{0,1\}^n \to \{0,1\}^n$ is the restriction of α to n.
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- We allow ¬ gates in relativized CC(α) circuits.
 (We can allow them in comparator circuits without changing CC.)
- Changing one input to one α_n gate produces a unique flip path in the circuit from that gate to the outputs of the circuit.

There is a relation $R_1(\alpha)$ computable by a polysize family of comparator oracle circuits by which cannot be computed by any NC(α) circuit family (even when α is restricted to be 1-Lipschitz).

Proof Idea.

• $\alpha_n^k(\vec{0})$ is easily computed by relativized comparator circuits, but requires depth k circuits [ACN 07].

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- $\alpha_n^k(\vec{0})$ is easily computed by relativized comparator circuits, but requires depth k circuits [ACN 07].
- The hard part is proving the depth lower bound when α is 1-Lipschitz.

There is a relation $R_2(\alpha)$ computable by an NC³(α) circuit family but not computable by any polysize family of comparator oracle circuits (even when α is restricted to be 1-Lipschitz).

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Proof Idea where α is weakly 1-Lipschitz

(At most one output bit flips when one input bit flips.)

• Let $a_i^k : \{0,1\}^{dn} \to \{0,1\}$ be a Boolean oracle.

• Let
$$A^k = (a_1^k, \ldots, a_n^k)$$

• Define a function $y = f[A^1, \ldots, A^m]$ as follows:

$$x_i^k = a_i^k (\overbrace{x_1^{k+1}, \dots, x_1^{k+1}}^{d \text{ times}}, \dots, \overbrace{x_n^{k+1}, \dots, x_n^{k+1}}^{d \text{ times}}), \quad k \in [m], \ i \in [n],$$
$$x_i^{m+1} = 0, \qquad \qquad i \in [n],$$
$$y = x_1^1 \oplus \dots \oplus x_n^1.$$



• a_i^k has *dn* inputs and one output.

- Add dn 1 zeros as extra outputs for each a_i^k .
- Each a_i^k computes a weakly 1-Lipschitz function.
- Let $X^k = (x_1^k, ..., x_n^k)$ $A^k = (a_1^k, ..., a_n^k)$
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- **Claim:** Every oracle comparator circuit computing $f[A^1, ..., A^m]$ has at least min $(2^n, (d-2)^{m-1})$ gates. (Superpolynomial size)

Every oracle comparator circuit computing $f[A^1, \ldots, A^m]$ has at least $\min(2^n, (d-2)^{m-1})$ gates.

Fix an oracle comparator circuit C computing $y = f[A^1, \ldots, A^m]$

Def'n: An input to an oracle aⁱ_k is *regular* if it has the form (b₁)^d ··· (b_n)^d.
 We say oracle a^k_i is *regular* if a^k_i(Z) = 0 for all irregular inputs Z.

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- Let g_k be the expected total number of active gates a_1^k, \ldots, a_n^k in C under a uniformly random *regular* setting of all oracles.

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 (because $y = x_1^1 \oplus \cdots \oplus x_n^1$)

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• It suffices to show $g_{k+1} \ge (d-2)(g_k - g/2^n)$
Proof idea of final Claim:

 $g_{k+1} \geq (d-2)(g_k - g/2^n)$

• Consequence of weakly 1-Lipschitz: If we change the definition of some gate a_i^k at its current input in C, this generates a unique flip-path which may end at some copy of some other gate, in which case we say that the latter gate *consumes* the flip-path.

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- Let G_1, \ldots, G_{2^n} be a Gray code listing all strings in $\{0, 1\}^n$, starting at $G_1 = X_{k+1}$. We change the definition of the output of A^{k+1} (at its active input) successively from G_1 to G_{2^n} and count the number of flip paths generated.
- The Claim follows because every time a particular a_i^k gate is updated from one active input to the next, it will absorb as least d 2 flip paths.

Conclusion

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- It has interesting complete problems.
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Open Problems:

Are any of the following problems in in CC?

Integer matrix powering?

All context free languages?

maximum matching in graphs?